

Compal confidential

Low Cost Los Angeles 10AL+

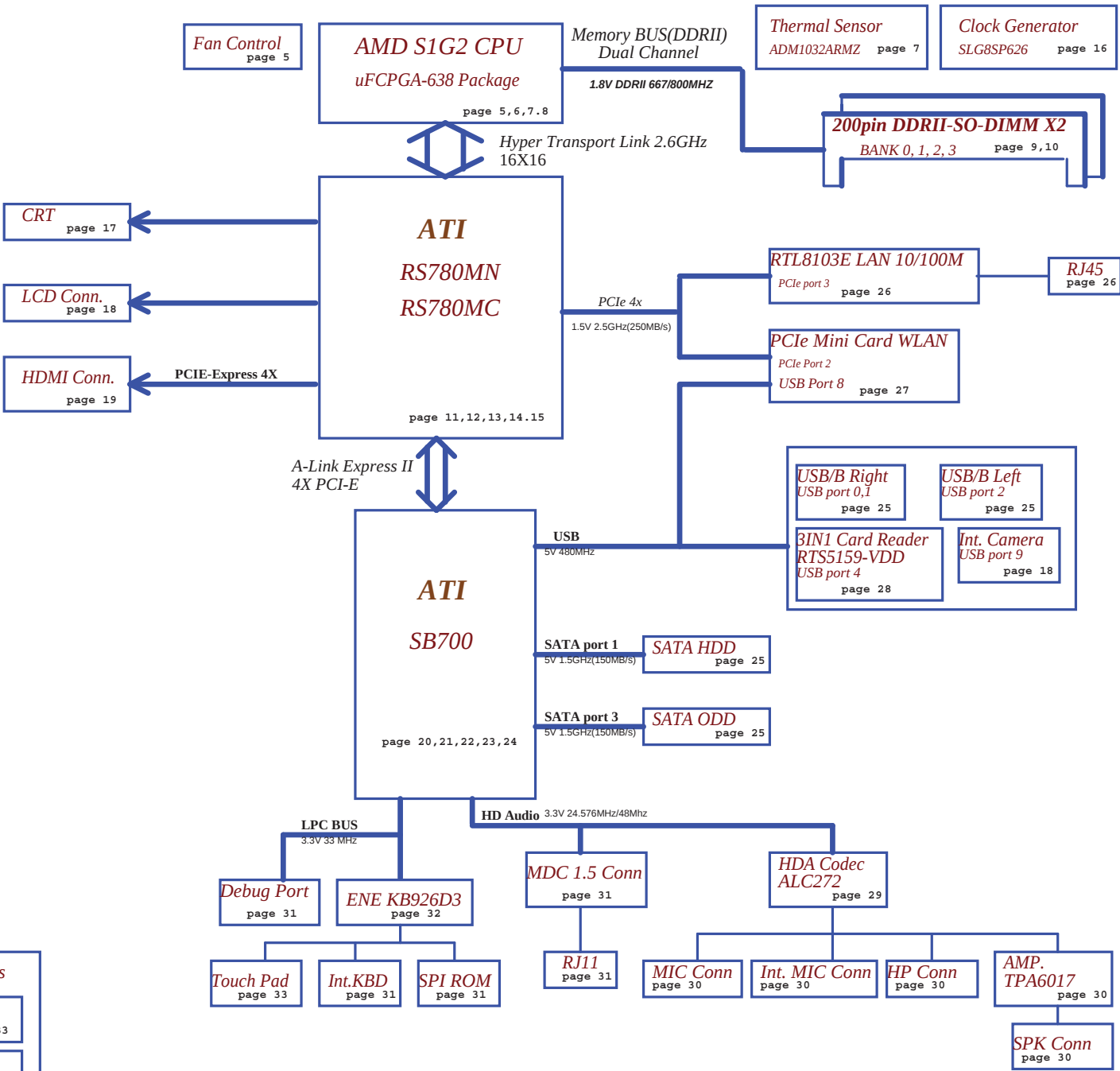
NBWAE LA-5831P Schematics Document

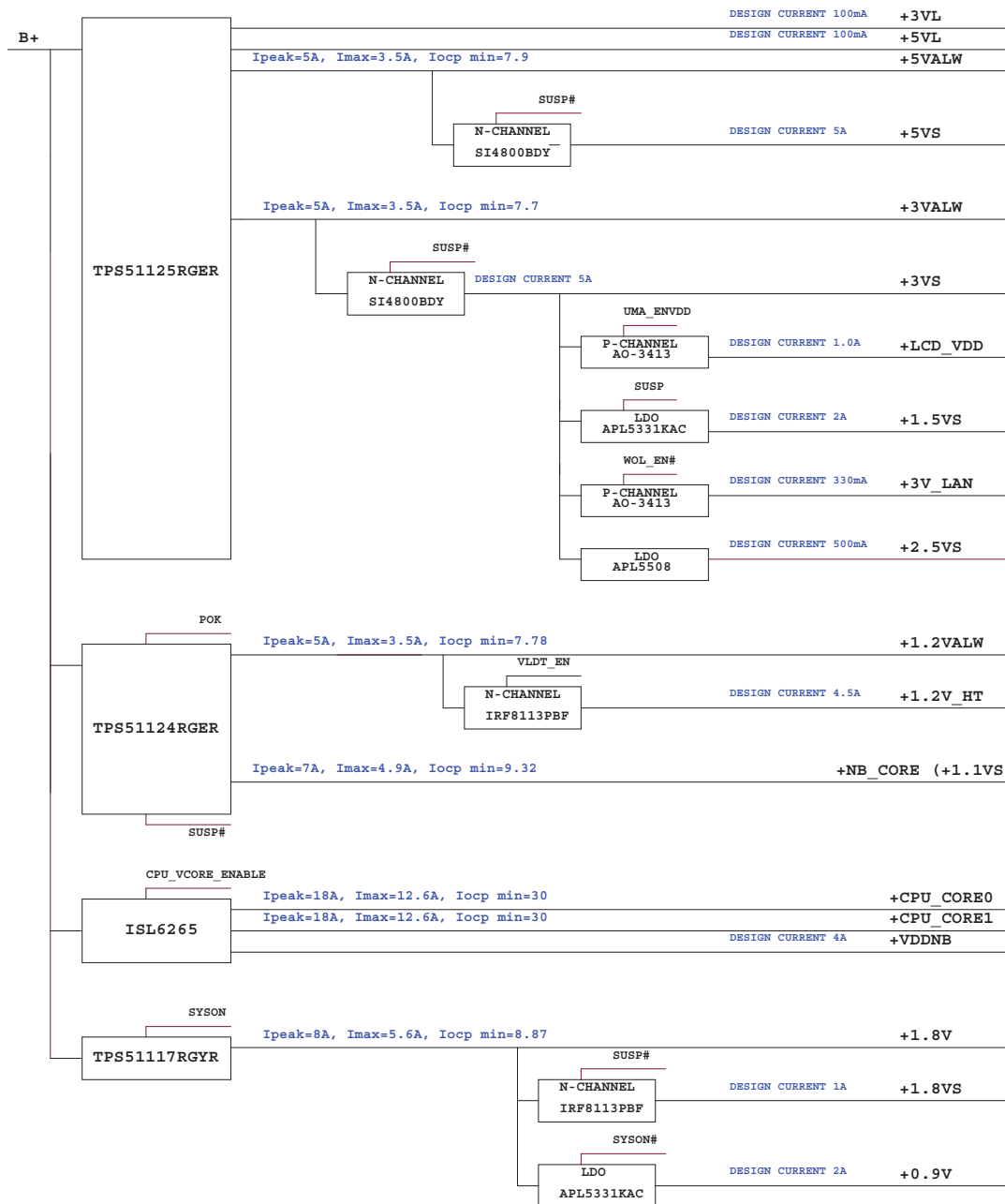
Mobile AMD S1G2

RS780MN & RS780MC / SB700

2009-08-12 Rev. 1.0

Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2009-02-12	Deciphered Date	2009-02-12	Cover Sheet	
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Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2009-02-12	Deciphered Date	2009-02-12	Title	Power Map
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Voltage Rails

O : ON
X : OFF

State \ power plane				
	B+ +3VL +5VL +RTCVCC	+5VALW +3VALW +1.2VALW +3V_LAN	+1.8V +0.9V	+5VS +3VS +2.5VS +1.8VS +1.5VS +1.1VS +1.2V_HT +VDDNB +CPU_CORE_0 +CPU_CORE_1
	S0	O	O	O
	S1	O	O	O
	S3	O	O	X
	S5 S4/AC	O	O	X
S5 S4/ Battery only	O	X	X	X
S5 S4/AC & Battery don't exist	X	X	X	X

SB700 SM Bus0 Address				SB700 SM Bus1 Address			
Power	Device	HEX	Address	Power	Device	HEX	Address
+3VS	DDR SO-DIMM 0	A0 H	1010 0000 b	+3VALW	WLAN/WIMAX		
+3VS	DDR SO-DIMM 1	A4 H	1010 0100 b				
+3VS	Clock Generator	D2 H	1101 0010 b				

KB926 SM Bus1 Address				KB926 SM Bus2 Address			
Power	Device	HEX	Address	Power	Device	HEX	Address
+3VL	Smart Battery	16 H	0001 011X b	+3VS	CPU_ADM1032-1	98 H	1001 100X b
KB926 ESB Address							
Power	Device	HEX	Address	Power	Device	HEX	Address
+3VL	Cap. Sensor				Virtual I2C		

Platform	CPU	NB	SB
PUMA@	S1G2	RS780MC	SB700
	S1G2	RS780MN	SB700
TIGRIS@	S1G3	RS880MC	SB710
	S1G3	RS880M	SB710

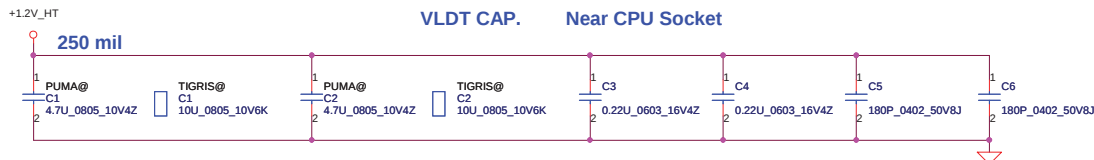
@ : just reserve , no build

BTO (Build-To-Order) Option Table

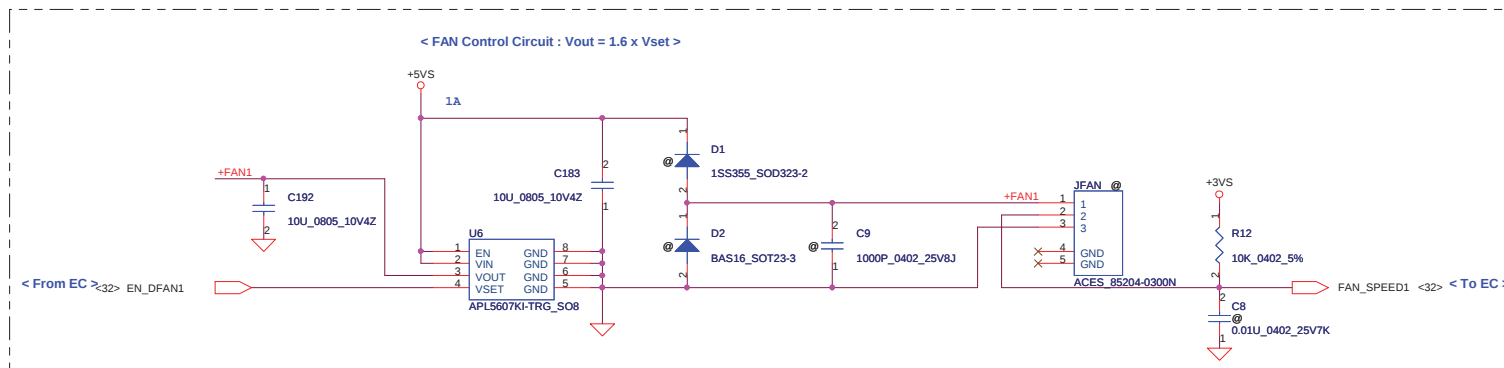
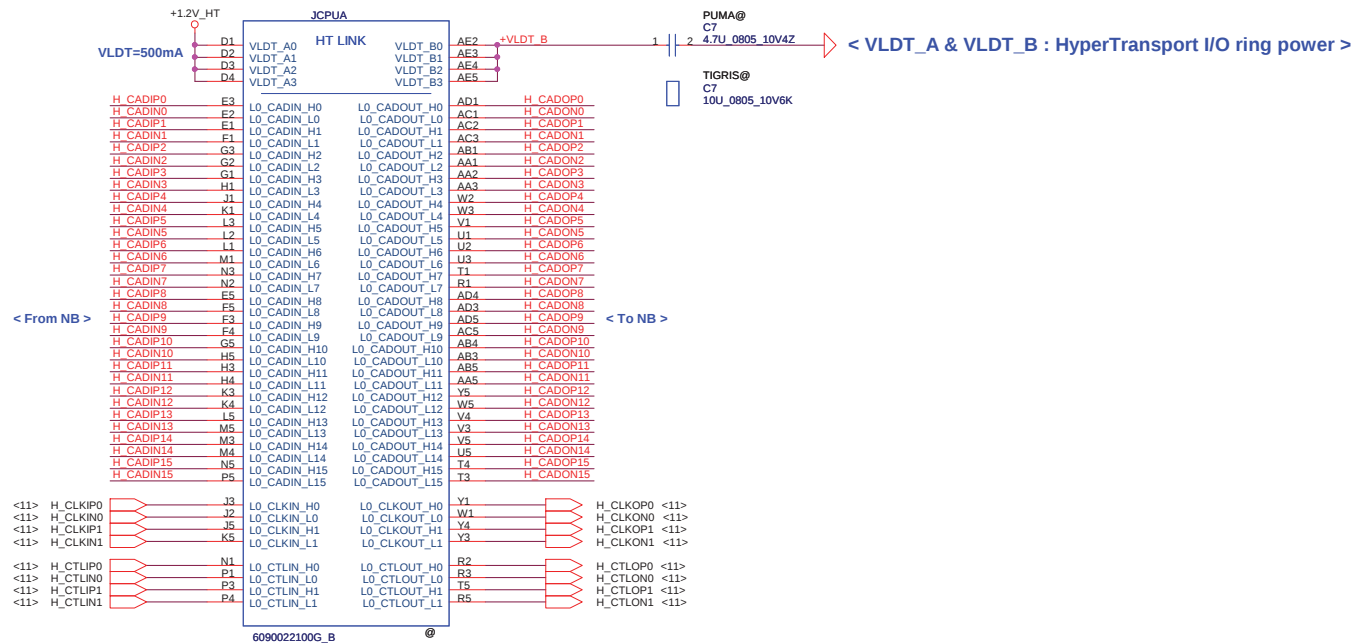
Function	Modem	HDMI	CAMERA & MIC	
Description	(R)	(Y)	(X)	
Explain			CAMERA	MIC
BTO	MDC@	HDMI@	CAM@	MIC@

SMBUS Control Table

	SOURCE	INVERTER	BATT	HDMI CEC	CPU THERMAL SENSOR	SODIMM I / II	CLK GEN	WLAN	LCD DDC ROM	HDMI DDC ROM	NEW CARD
EC_SMB_CK1 EC_SMB_DA1	KB926		V								
EC_SMB_CK2 EC_SMB_DA2	KB926				V						
I2C_CLK I2C_DATA	RS780MN RS780MC								V		
DDC_CLK0 DDC_DATA0	RS780MN RS780MC									V	
DDC_CLK1 DDC_DATA1	RS780MN RS780MC										
SCL0 SDA0	SB700					V	V				
SCL1 SDA1	SB700							V			
SCL2 SDA2	SB700										
SCL3 SDA3	SB700										

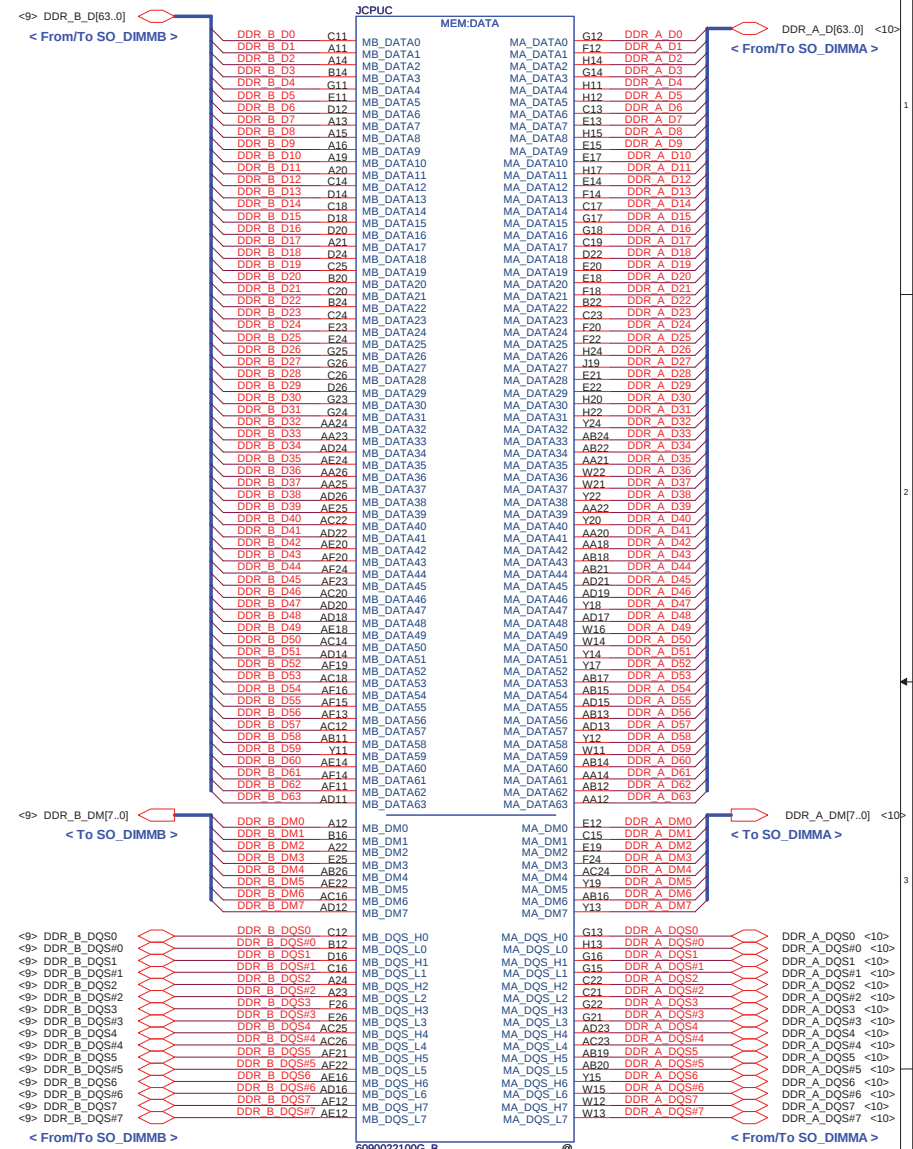
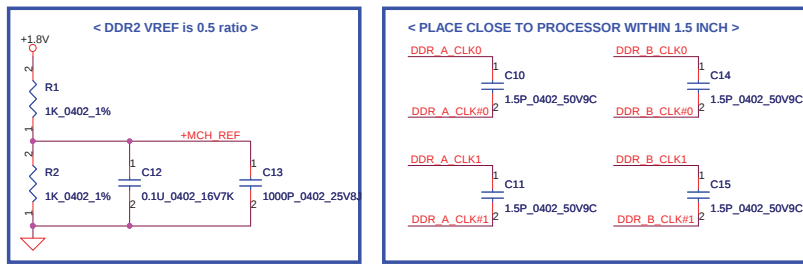


<11> H_CADIP[0..15] H_CADIP[0..15] H_CADOP[0..15] <11>
 <11> H_CADIN[0..15] H_CADIN[0..15] H_CADON[0..15] <11>

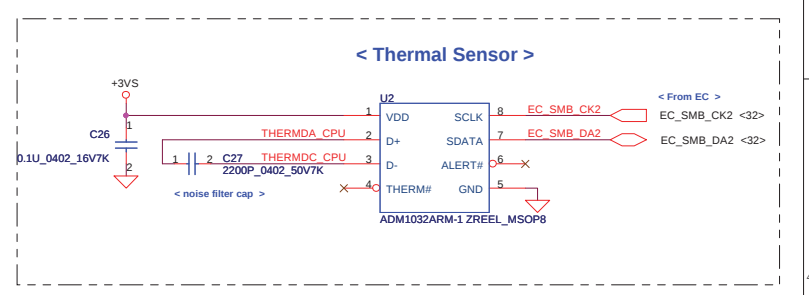
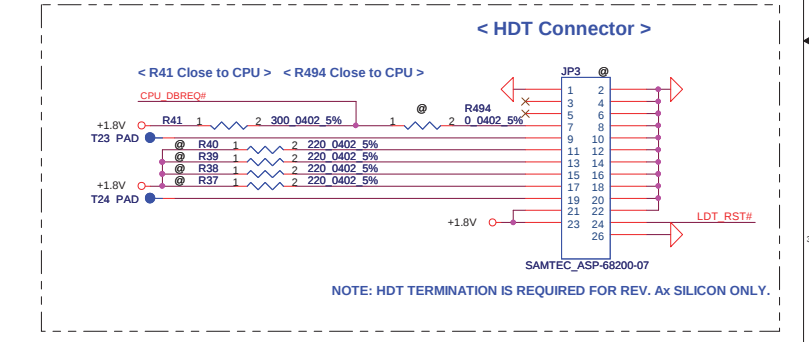
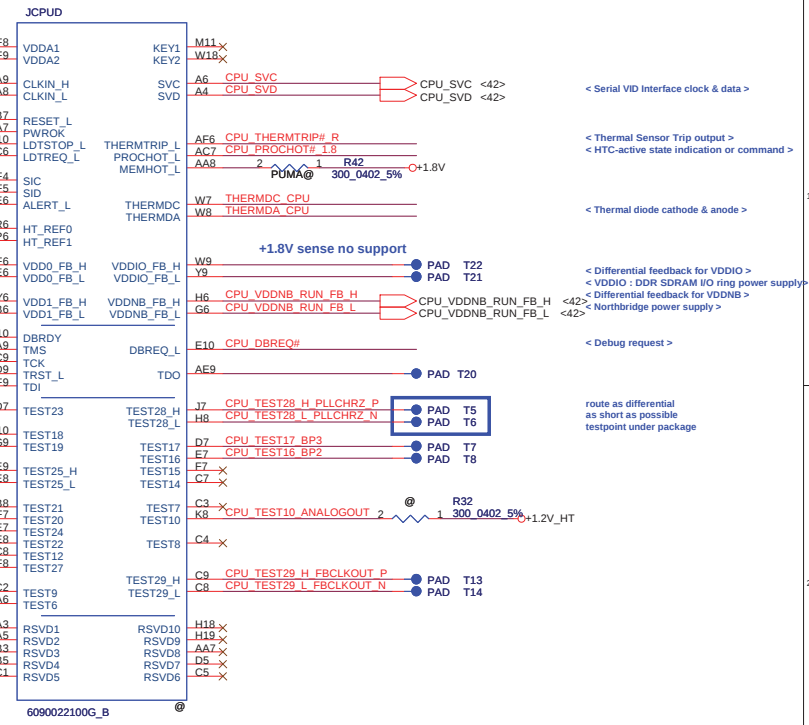
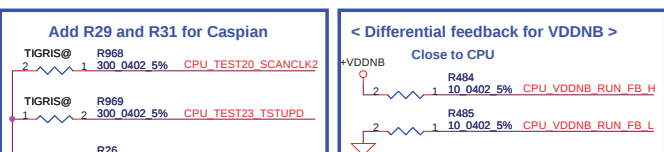
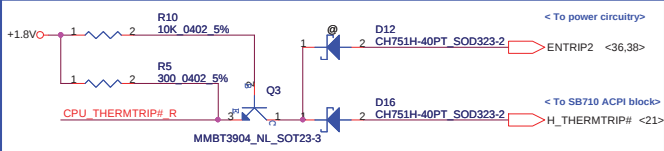
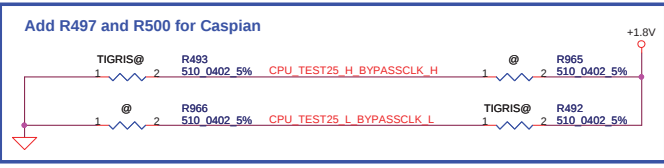
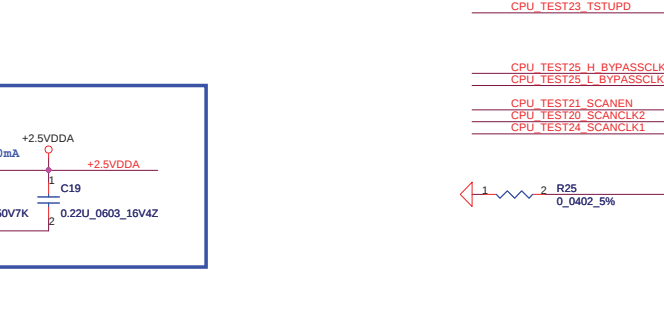
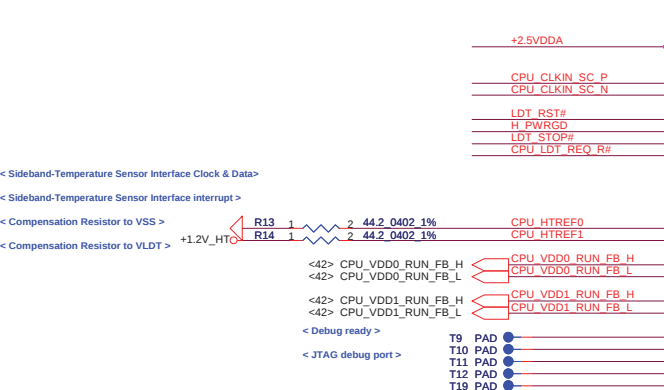
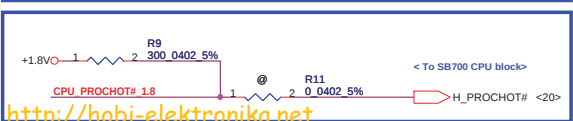
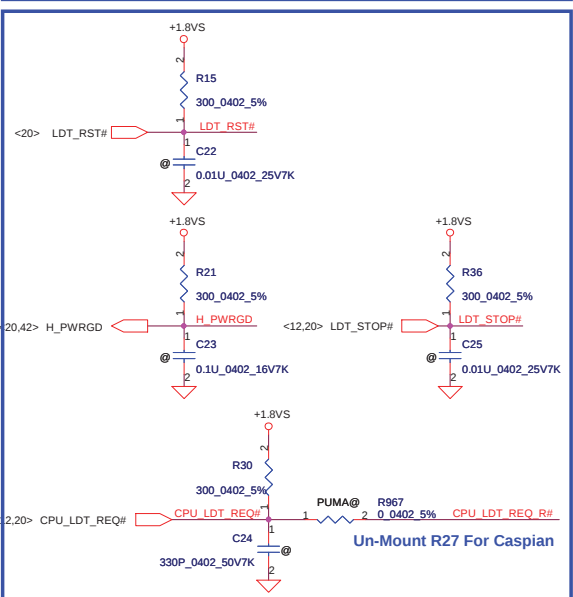
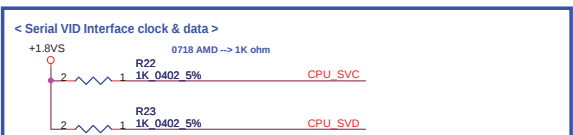
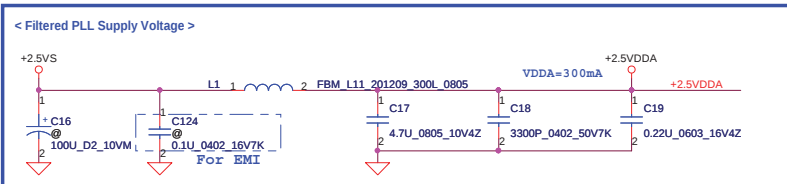
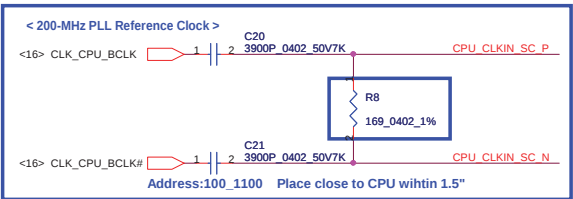
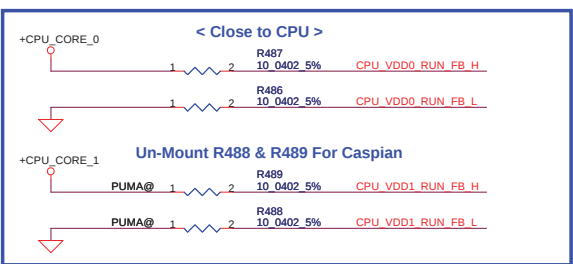


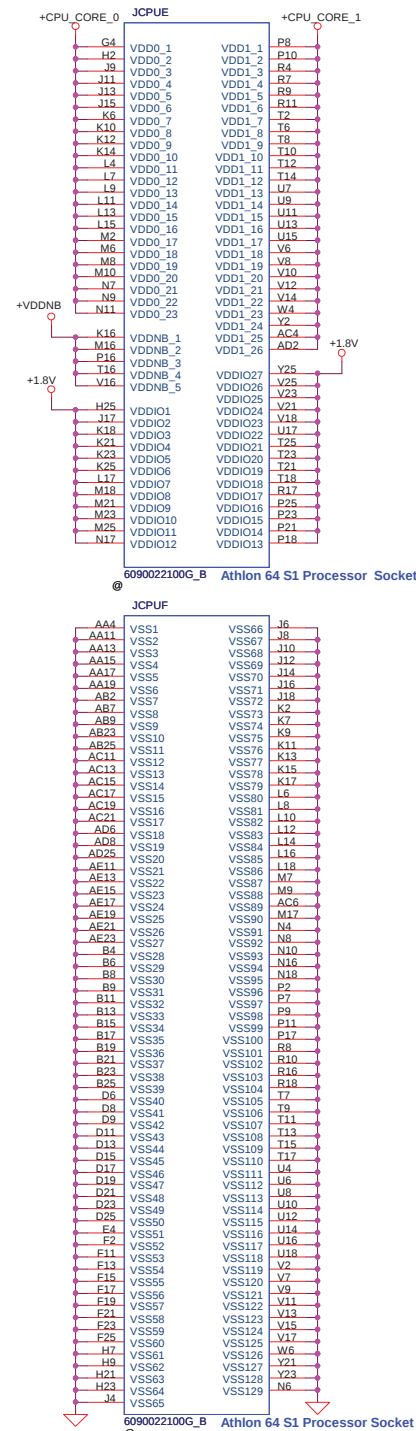
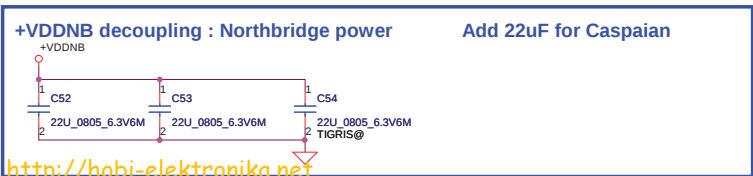
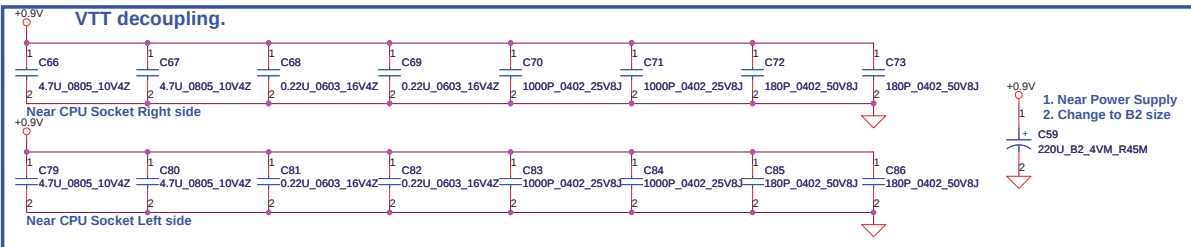
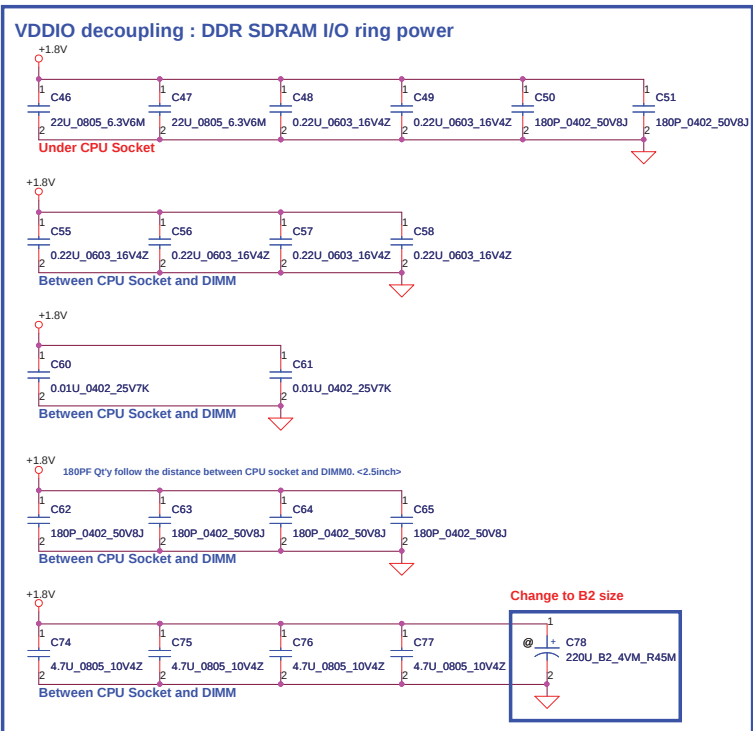
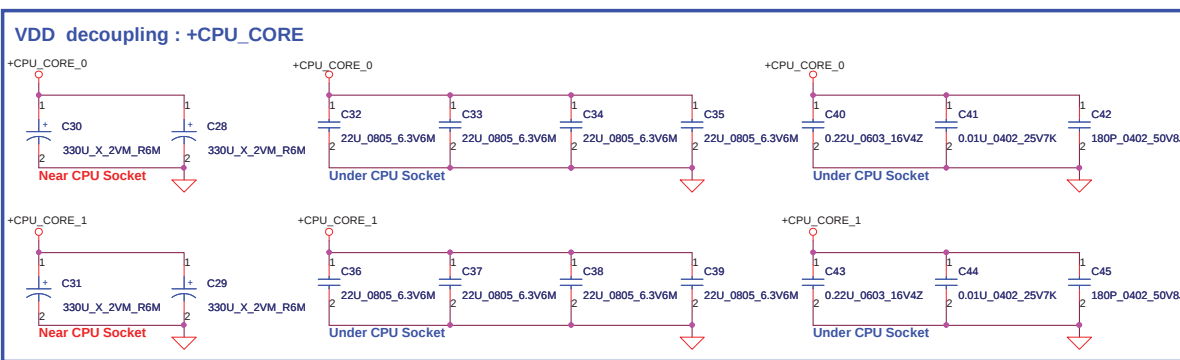
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Compal Electronics, Inc.				LA-5831P
AMD CPU S1G3 HT I/F				Wednesday, August 12, 2009
Date				Sheet 5 of 44

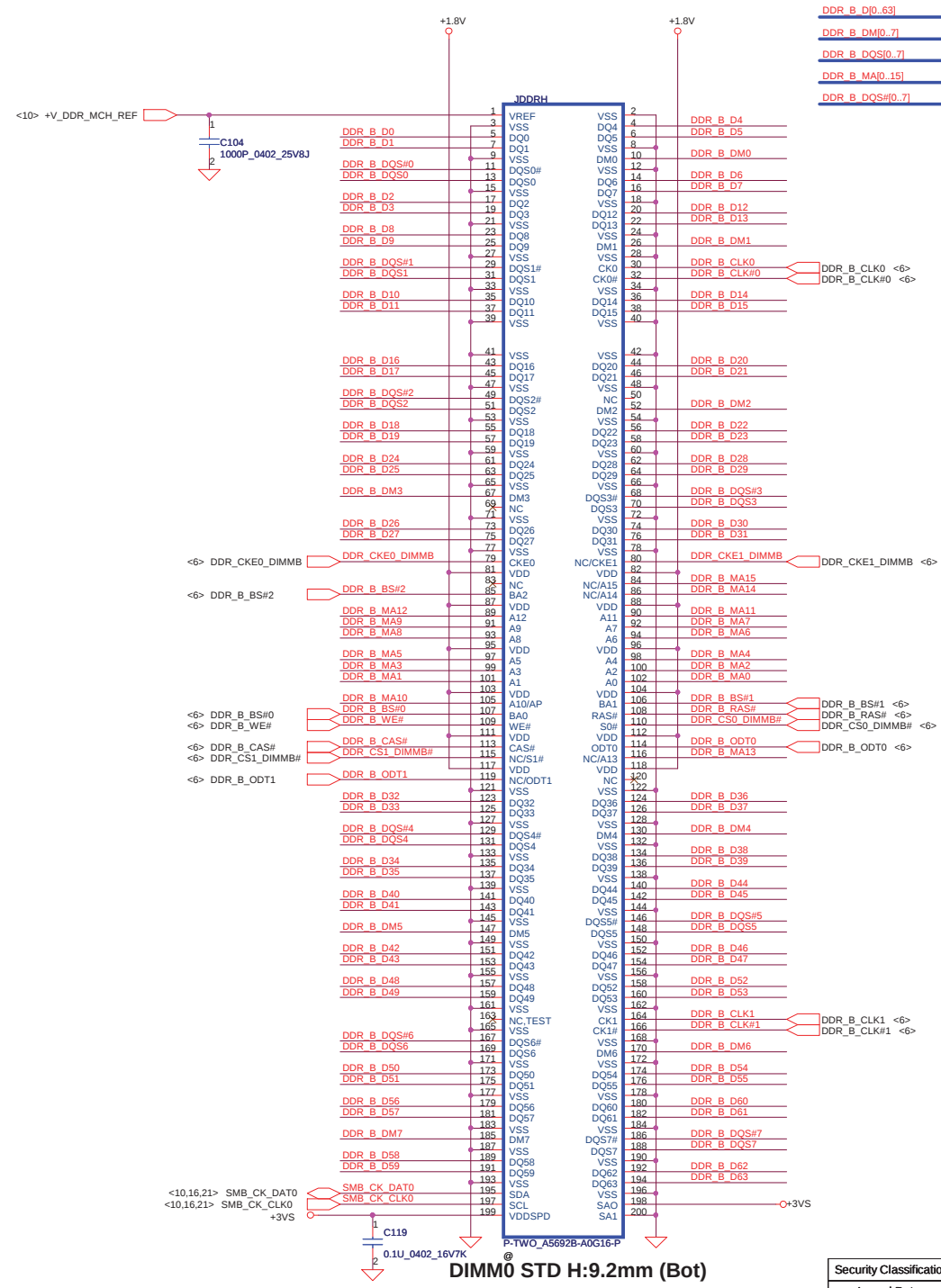
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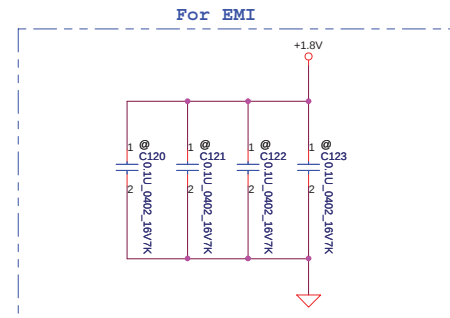
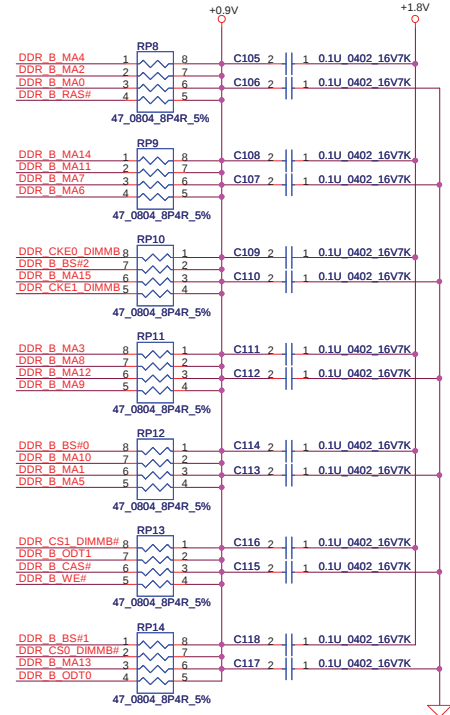
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Issued Date	2009-02-12	Deciphered Date	2009-02-12	Title			
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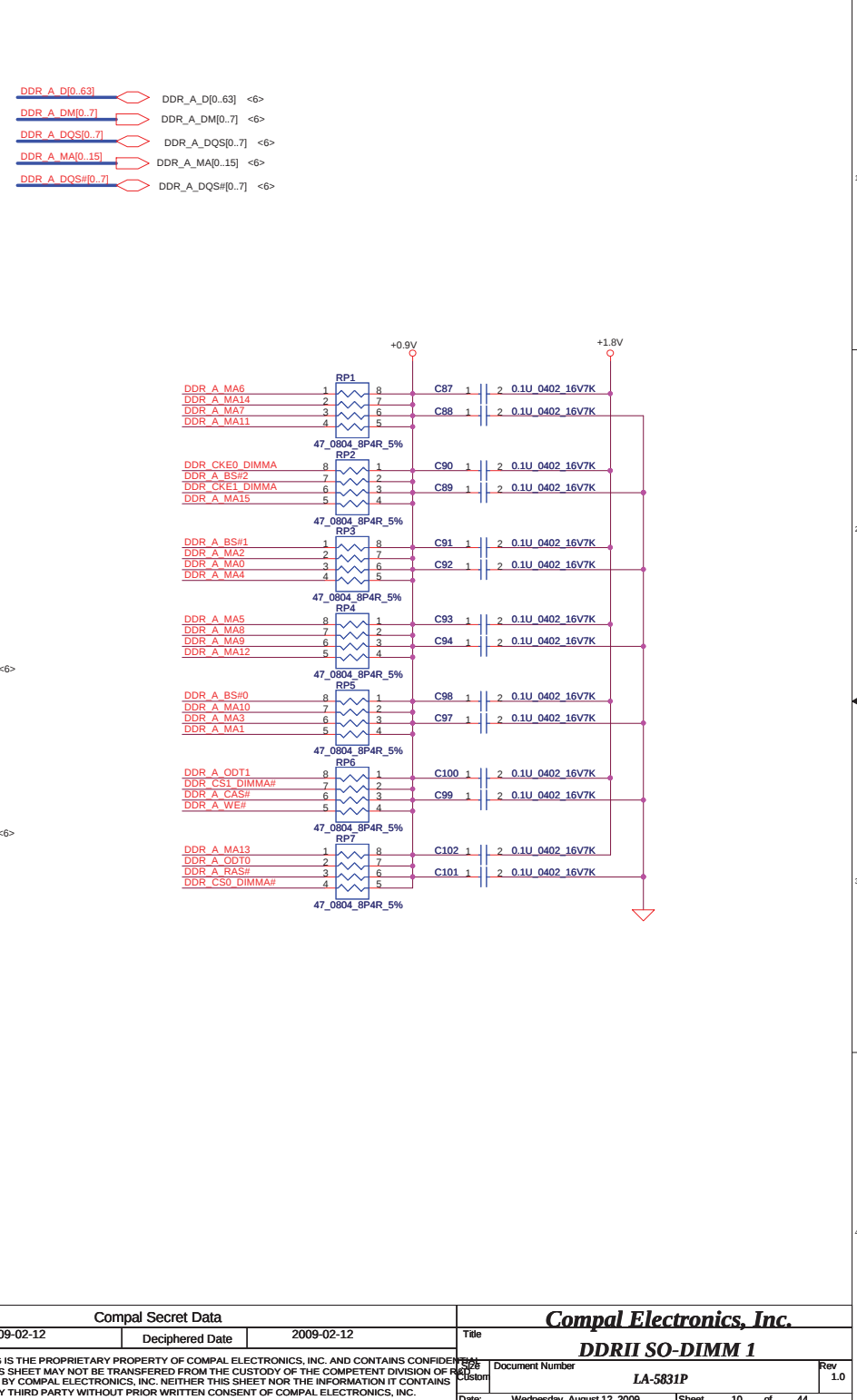
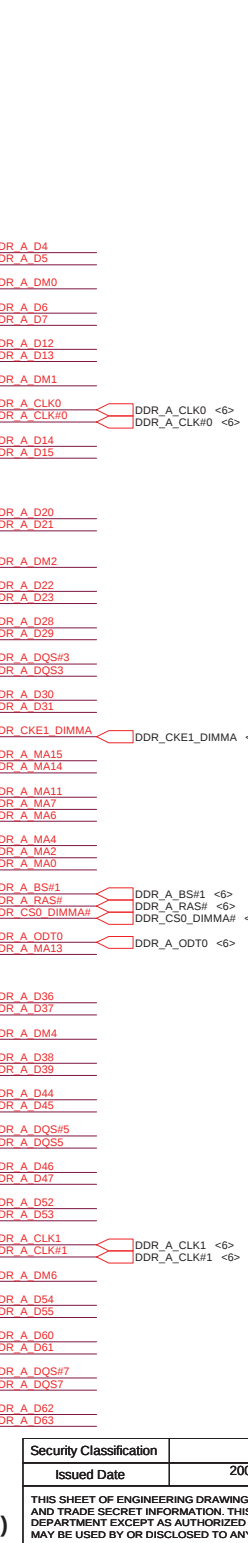
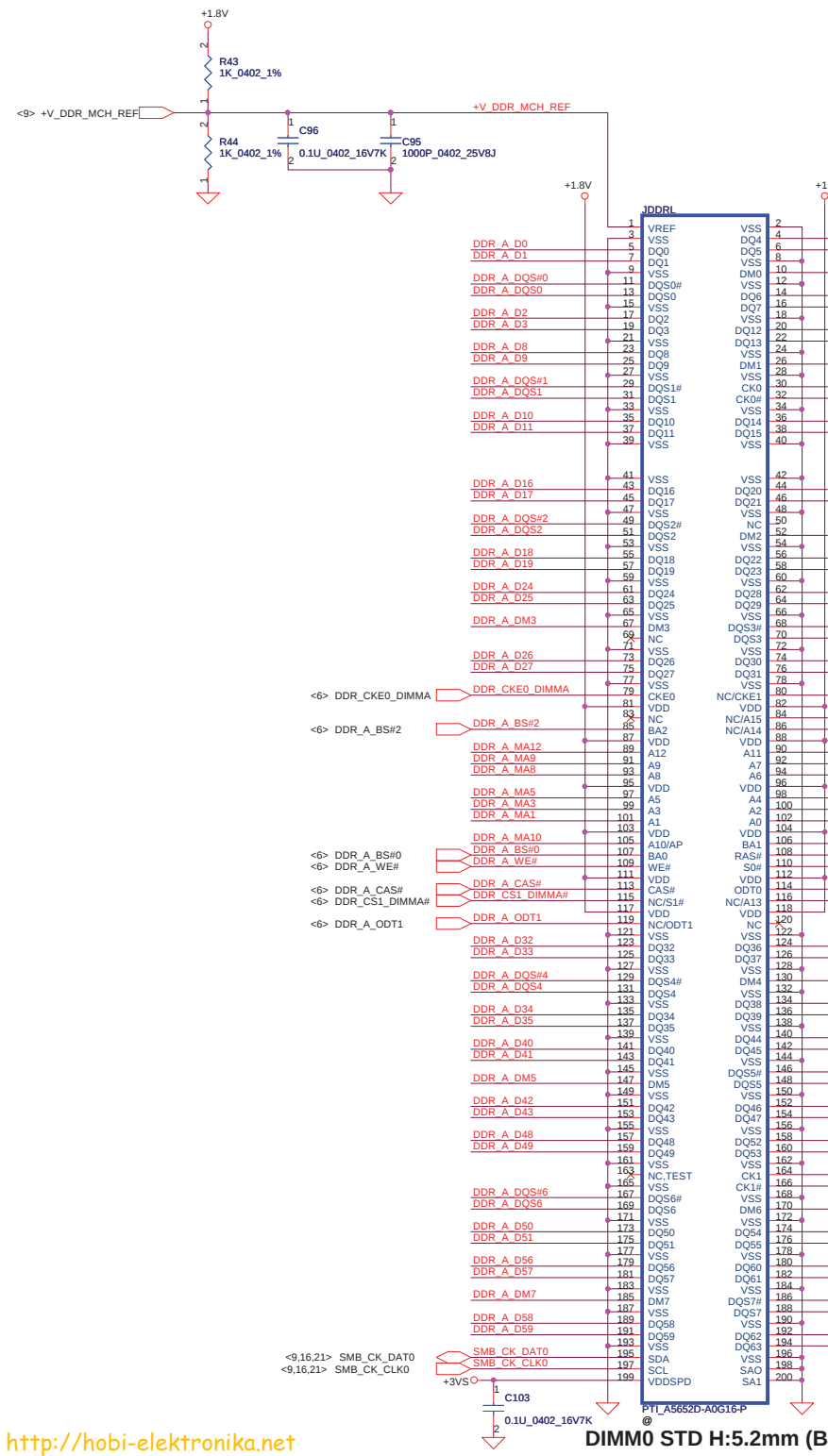


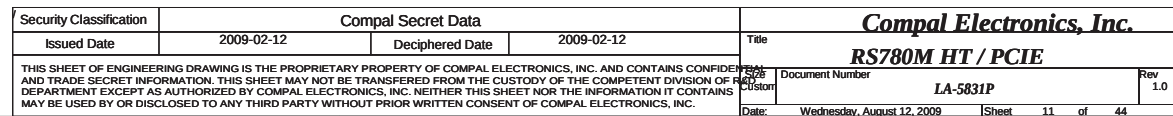


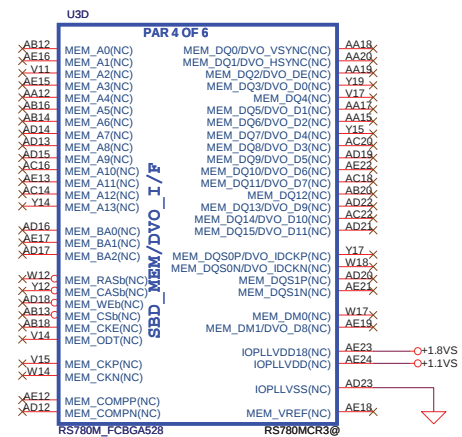


DDR_B D[0..63] <6>
DDR_B DM[0..7] <6>
DDR_B DQS[0..7] <6>
DDR_B MA[0..15] <6>
DDR_B DQS#[0..7] <6>

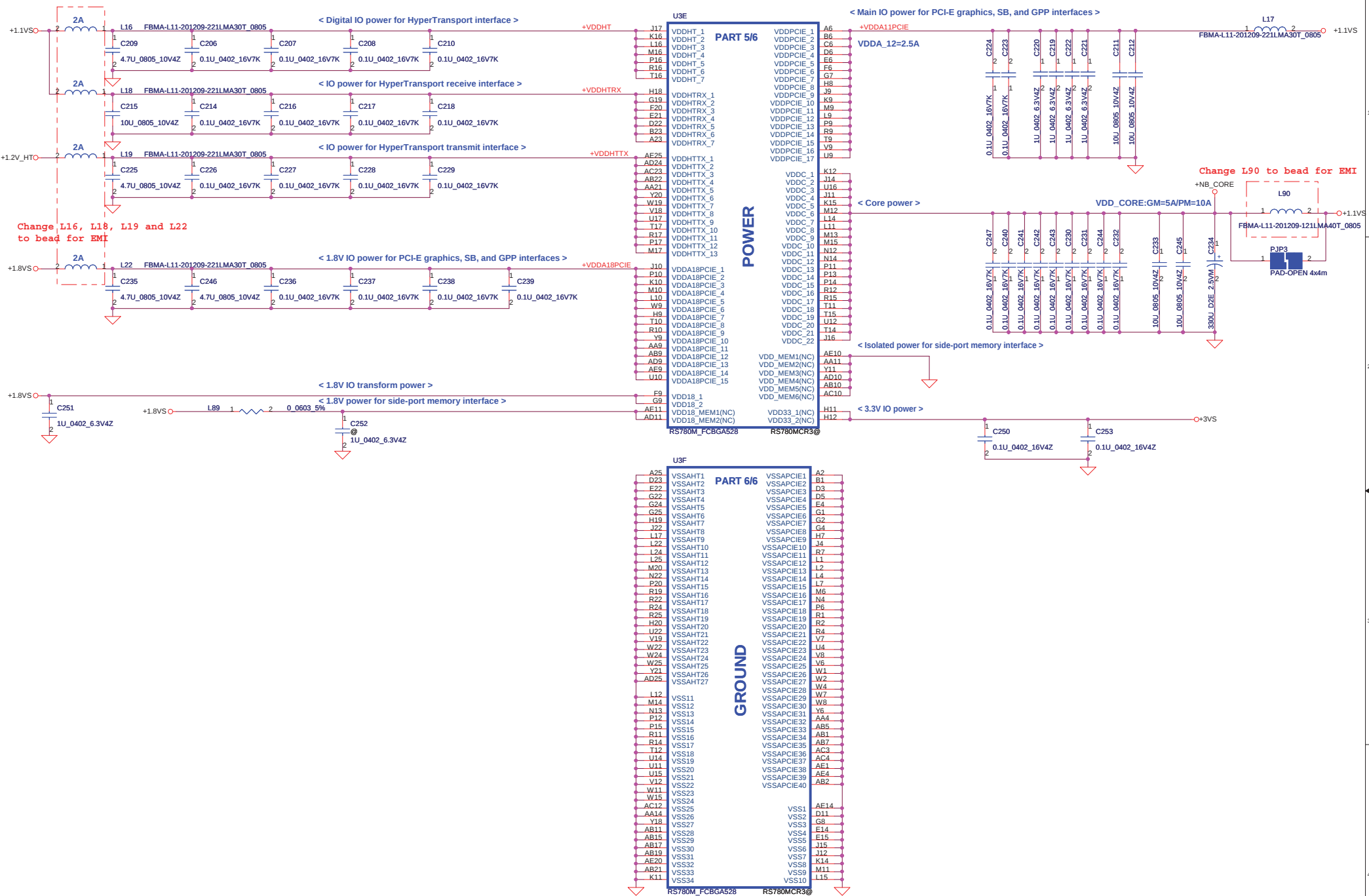






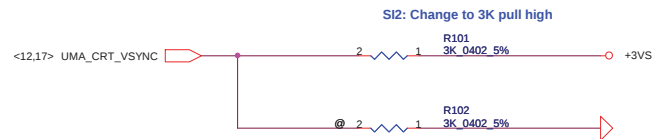


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Issued Date	2008/04/14	Deciphered Date	2009/04/14	Title		
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Issued Date	2009-02-12	Deciphered Date	2009-02-12	RS780M PWR / GND	
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< RS780 DFT_GPIO5 mux at CRT_VSYNC pull High to 3K >



< DFT_GPIO5:STRAP_DEBUG_BUS_GPIO_ENABLEb >

Enables the Test Debug Bus using GPIO.

1 : Enable (RX780, RS780)
0 : Disable (RX780, RS780)

PIN: RS740-->RS780_AUX_CAL; RX780-->NB_TV_C; RS780--> VSYNC#

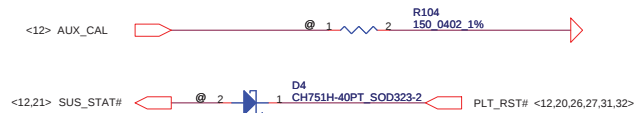
< RS780 use register to control PCI-E configure >

< DFT_GPIO[4:2] : STRAP_PCIE_GPP_CFG[2:0] >

These pin straps are used to configure PCI-E GPP mode.

000 : 00001
001 : 00010
010 : 01011
011 : 00100
100 : 01010
101 : 01100
111 : 01011

< RS780 DFT_GPIO1 >



< DFT_GPIO1 : LOAD_EEPROM_STRAPS >

Selects Loading of STRAPS from EPROM

1 : Bypass the loading of EEPROM straps and use Hardware Default Values
0 : I2C Master can load strap values from EEPROM if connected, or use default values if not connected

RS740/RX780: DFT_GPIO1 RS780:SUS_STAT

< RS780 use HSYNC to enable SIDE PORT (internal pull high) >



< DFT_GPIO0: STRAP_DEBUG_BUS_PCIE_ENABLEb >

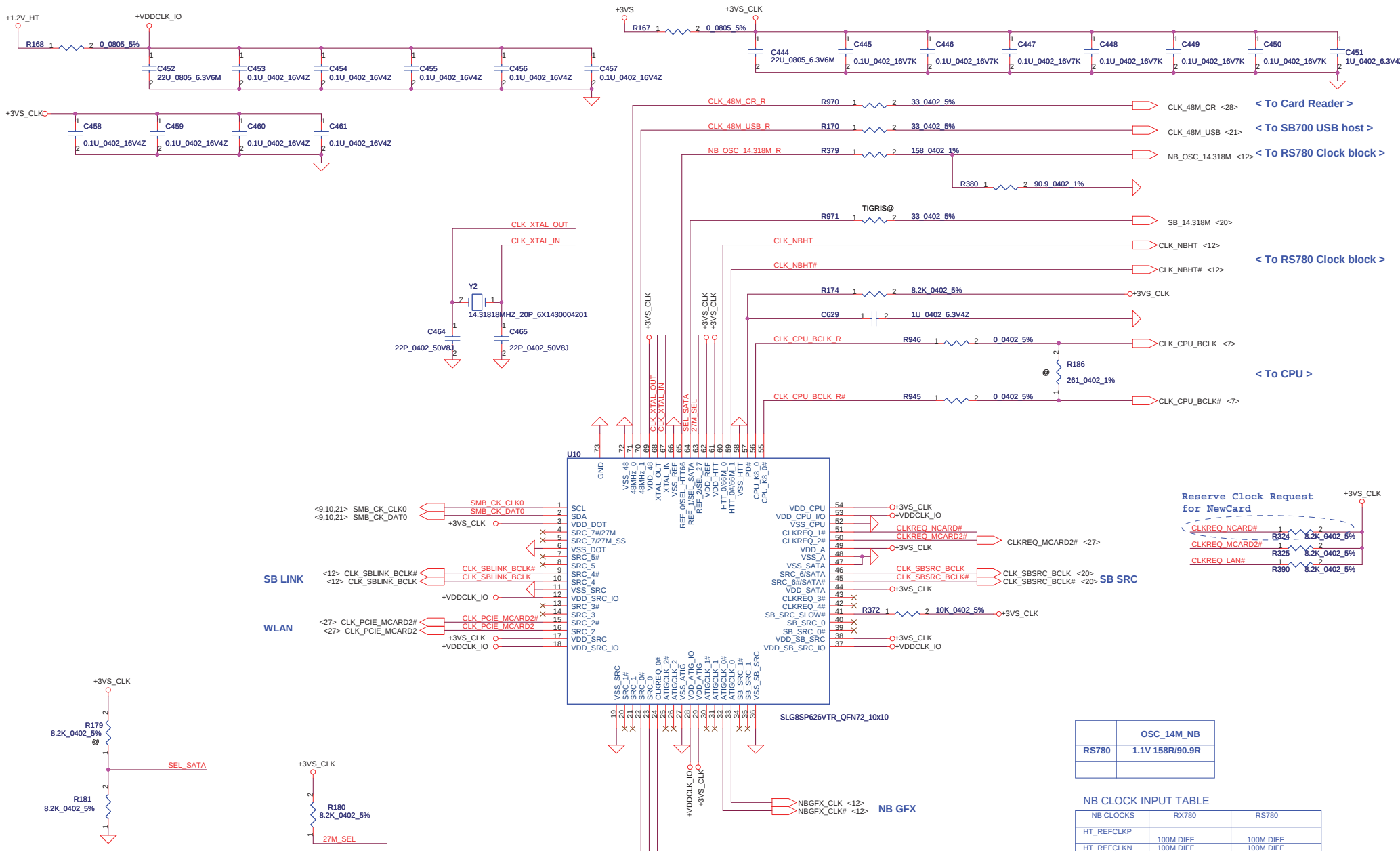
RX780: Enables the Test Debug Bus using PCIE bus

1 : Disable (Can still be enabled using nbcfg register access)
0 : Enable

RS780: Enables Side port memory (RS780 use HSYNC#)

1. Disable (RS780)
0 : Enable (RS780)

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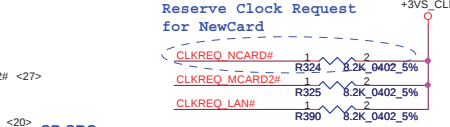


SEL_SATA	1	configure as SATA output
	0 *	configure as normal SRC(SRC_6) output * default

27M_SEL	1 *	configure as 27M and 27M_SS output
	0	configure as SRC_7 output * default

NB_OSC_14.318M	1	configure as single-ended 66MHz output
	0*	configure as differential 100MHz output * default

<http://hobi-elektronika.net>



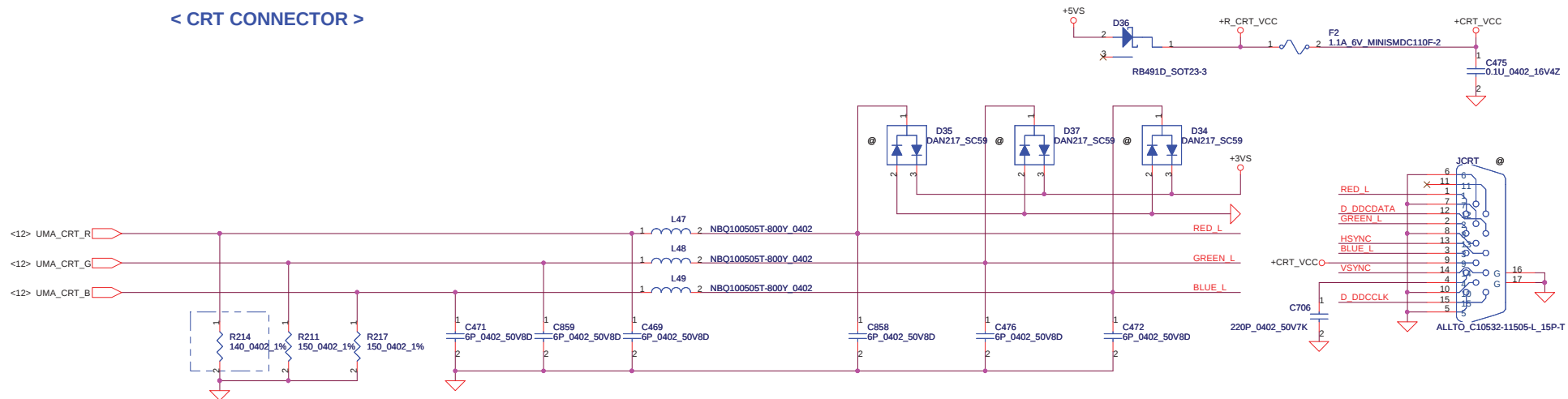
	OSC_14M_NB
RS780	1.1V 158R/90.9R

NB CLOCK INPUT TABLE

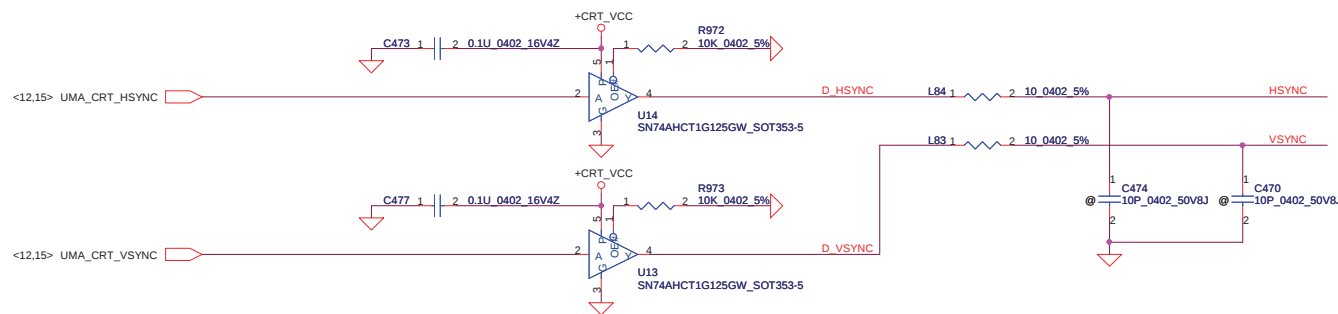
NB CLOCKS	RX780	RS780
HT_REFCLKP	100M DIFF	100M DIFF
HT_REFCLKN	100M DIFF	100M DIFF
REFCLK_P	14M SE (1.8V)	14M SE (1.1V)
REFCLK_N	NC	vref
GFX_REFCLK	100M DIFF	100M DIFF(IN/OUT)*
GPP_REFCLK	100M DIFF	NC or 100M DIFF OUTPUT
GPPSB_REFCLK	100M DIFF	100M DIFF

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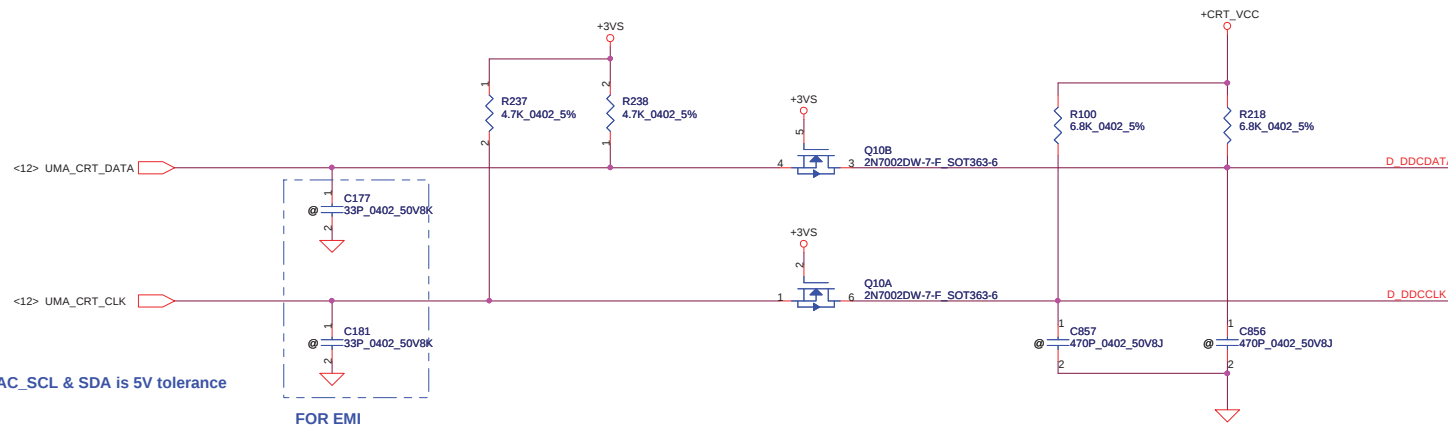
< CRT CONNECTOR >



< SYNC SIGNAL >



< Display Data Channel >

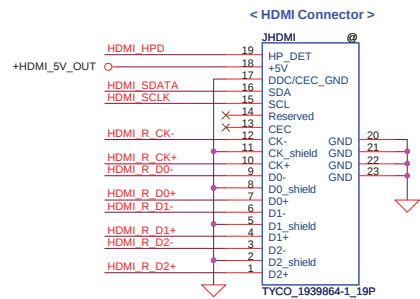


RS780 DAC_SCL & SDA is 5V tolerance

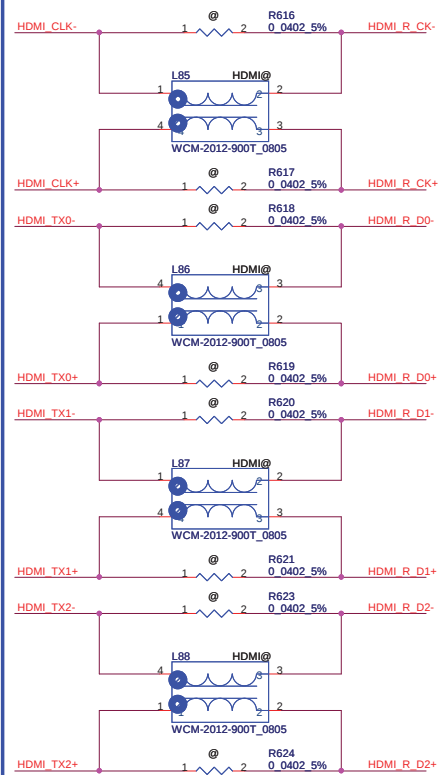
FOR EMI

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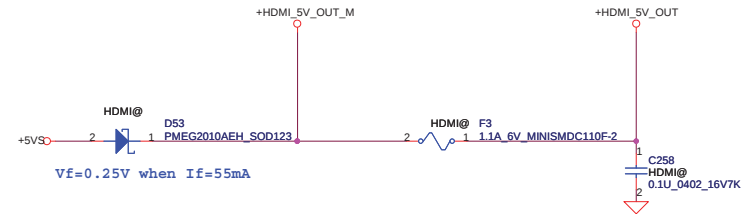
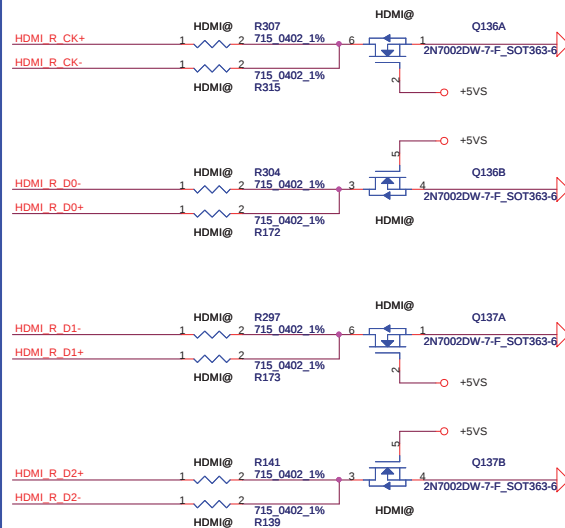
<11> HDMI_TXD0+	HDMI@	C189	1	2	0.1U_0402_16V7K	HDMI_TX0+
<11> HDMI_TXD0-	HDMI@	C188	1	2	0.1U_0402_16V7K	HDMI_TX0-
<11> HDMI_TXD1+	HDMI@	C190	1	2	0.1U_0402_16V7K	HDMI_TX1+
<11> HDMI_TXD1-	HDMI@	C184	1	2	0.1U_0402_16V7K	HDMI_TX1-
<11> HDMI_TXD2+	HDMI@	C187	1	2	0.1U_0402_16V7K	HDMI_TX2+
<11> HDMI_TXD2-	HDMI@	C191	1	2	0.1U_0402_16V7K	HDMI_TX2-
<11> HDMI_CLK0+	HDMI@	C185	1	2	0.1U_0402_16V7K	HDMI_CLK+
<11> HDMI_CLK0-	HDMI@	C186	1	2	0.1U_0402_16V7K	HDMI_CLK-



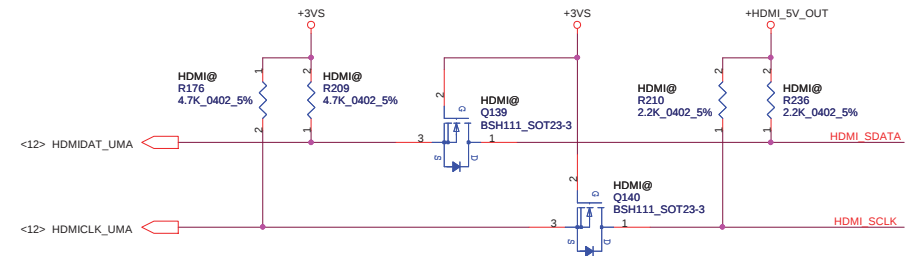
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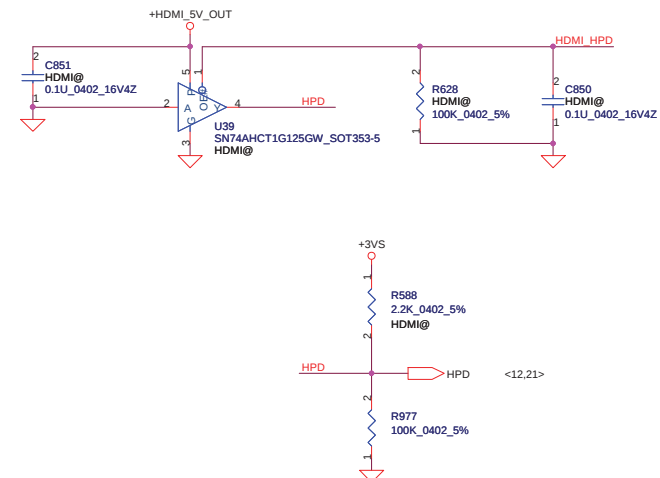
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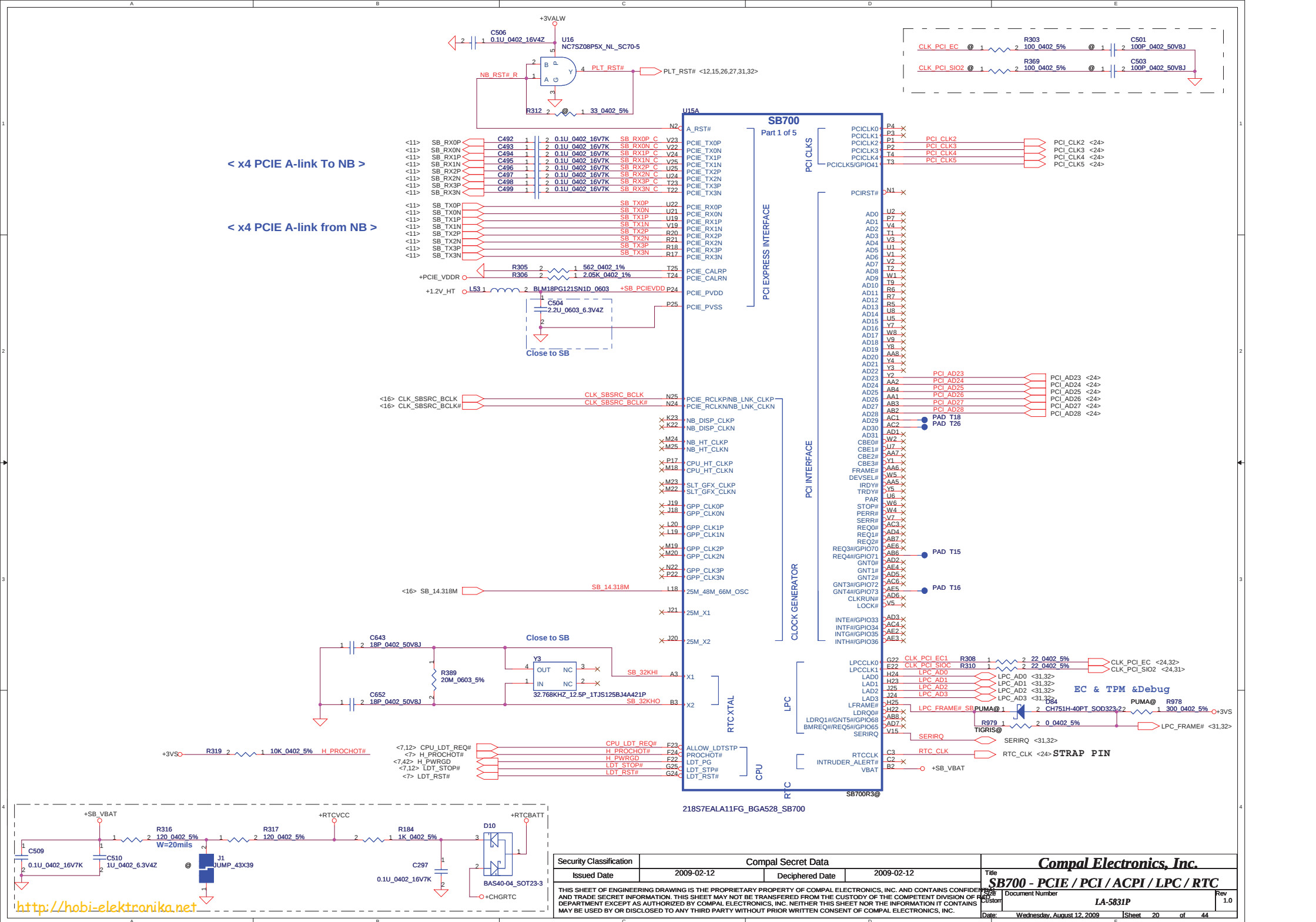
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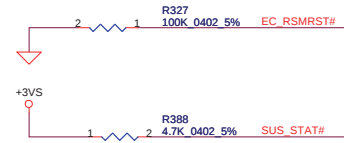
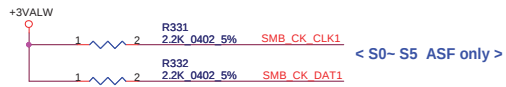
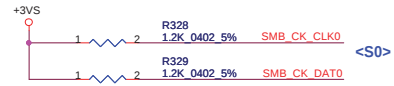
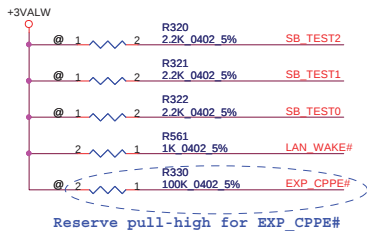


< Hot-plug detection & level shift >



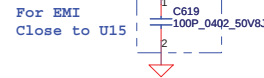
Security Classification		Compal Secret Data		Title	
Issued Date	2009-02-12	Deciphered Date	2009-02-12	HDMI	
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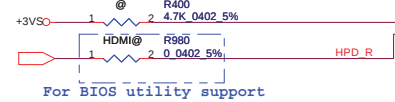
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 <31> HDA_BITCLK_MDC
 <31> HDA_SDOUT_MDC
 <29> HDA_SDOUT_CODECS
 <29> HDA_SDIW0
 <31> HDA_SDIW1
 <31> HDA_SYNC_MDC
 <29> HDA_SYNC_CODECS
 <29> HDA_RST#_CODECS
 <31> HDA_RST#_MDC
 <29> HDA_RST#

STRAP PIN<24>



<32> PM_SLP_S3#
 <32> PM_SLP_S5#
 <32> PBTN_OUT#
 <32,42> SB_PWRGD
 <12,15> SUS_STAT#
 <32> GATEA20
 <32> KB_RST#
 <32> EC_SC#
 <32> EC_SM#
 <26> LAN_WAKE#
 <7> H_THERMTRIP#
 <12> NB_PWRGD
 <32> EC_RSMRST#

<29> SB_SPKR
 <9,10,16> SMB_CK_CLK0
 <9,10,16> SMB_CK_DAT0
 <27> SMB_CK_CLK1
 <27> SMB_CK_DAT1



<32> EC_LID_OUT#
 <25,32> USB_OC#2
 <25,32> USB_OC#0

R333 1 2 33 0402 5%
 R334 1 MDC 2 33 0402 5%
 R335 1 MDC 2 33 0402 5%
 R336 1 MDC 2 33 0402 5%

R337 1 MDC 2 33 0402 5%
 R338 1 MDC 2 33 0402 5%
 R339 1 MDC 2 33 0402 5%
 R340 1 MDC 2 33 0402 5%

U15D
 SB700
 Part 4 of 5
 USBCLK/14M_25M_48M_OSC
 USB_RCOMP
 USB_FSD13P
 USB_FSD13N
 USB_FSD12P
 USB_FSD12N
 USB_HSD11P
 USB_HSD11N
 USB_HSD10P
 USB_HSD10N
 USB_HSD9P
 USB_HSD9N
 USB_HSD8P
 USB_HSD8N
 USB_HSD7P
 USB_HSD7N
 USB_HSD6P
 USB_HSD6N
 USB_HSD5P
 USB_HSD5N
 USB_HSD4P
 USB_HSD4N
 USB_HSD3P
 USB_HSD3N
 USB_HSD2P
 USB_HSD2N
 USB_HSD1P
 USB_HSD1N
 USB_HSD0P
 USB_HSD0N
 IMC_GPIO8
 IMC_GPIO9
 IMC_PWM0/IMC_GPIO10
 SCL2/IMC_GPIO11
 SDA2/IMC_GPIO12
 SCL3_LVIMC_GPIO13
 SDA3_LVIMC_GPIO14
 IMC_PWM1/IMC_GPIO15
 IMC_PWM2/IMC_GPIO16
 IMC_PWM3/IMC_GPIO17
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 IMC_GPIO39
 IMC_GPIO40
 IMC_GPIO41

SB700
 Part 4 of 5
 USBCLK/14M_25M_48M_OSC
 USB_RCOMP
 USB_FSD13P
 USB_FSD13N
 USB_FSD12P
 USB_FSD12N
 USB_HSD11P
 USB_HSD11N
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 IMC_GPIO9
 IMC_PWM0/IMC_GPIO10
 SCL2/IMC_GPIO11
 SDA2/IMC_GPIO12
 SCL3_LVIMC_GPIO13
 SDA3_LVIMC_GPIO14
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 IMC_GPIO40
 IMC_GPIO41

SB700
 Part 4 of 5
 USBCLK/14M_25M_48M_OSC
 USB_RCOMP
 USB_FSD13P
 USB_FSD13N
 USB_FSD12P
 USB_FSD12N
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 USB_HSD10P
 USB_HSD10N
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 SDA2/IMC_GPIO12
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 IMC_GPIO41

SB700
 Part 4 of 5
 USBCLK/14M_25M_48M_OSC
 USB_RCOMP
 USB_FSD13P
 USB_FSD13N
 USB_FSD12P
 USB_FSD12N
 USB_HSD11P
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 USB_HSD10P
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 IMC_GPIO9
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 SCL2/IMC_GPIO11
 SDA2/IMC_GPIO12
 SCL3_LVIMC_GPIO13
 SDA3_LVIMC_GPIO14
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 IMC_GPIO41

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				Wednesday, August 12, 2009	Sheet 21 of 44

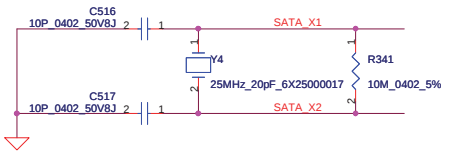
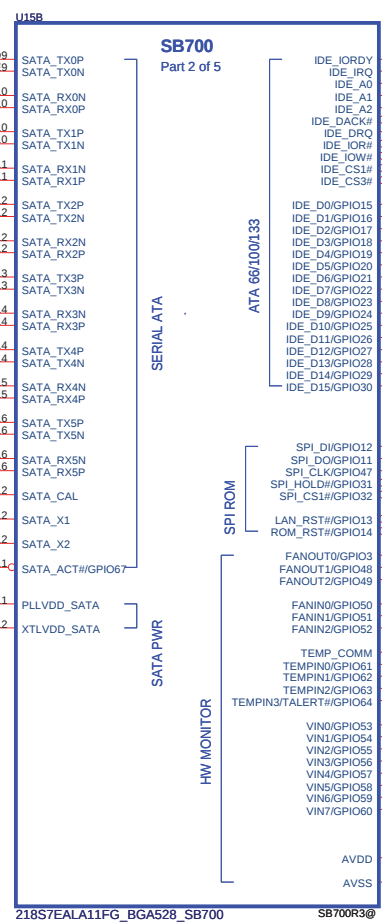
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ODD

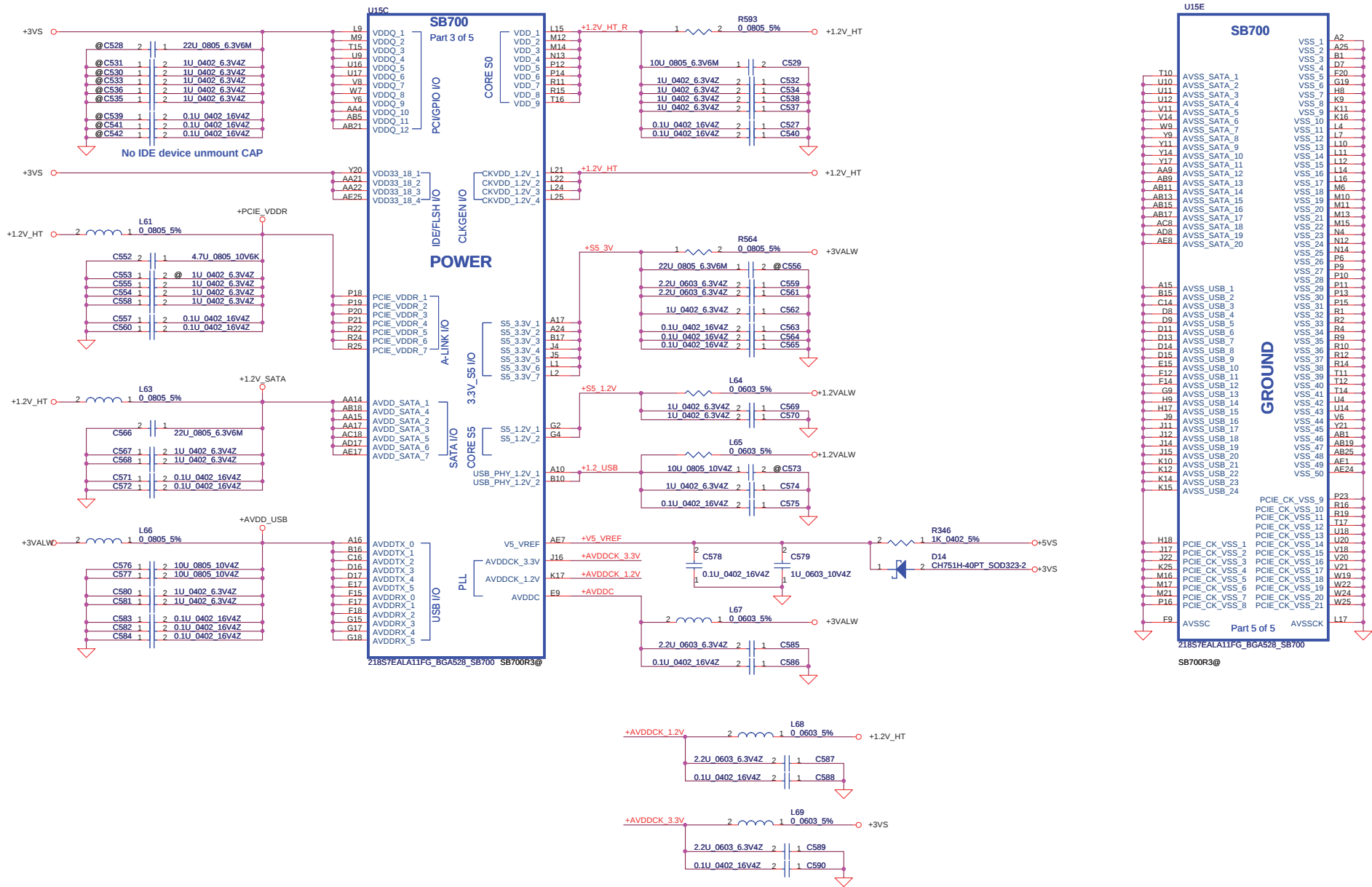
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3

4



Security Classification		Compal Secret Data		Title	
Issued Date	2009-02-12	Deciphered Date	2009-02-12	SB700 SATA / IDE / SPI	
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				LA-5831P	1.0
Date: Wednesday, August 12, 2009				Sheet	22 of 44

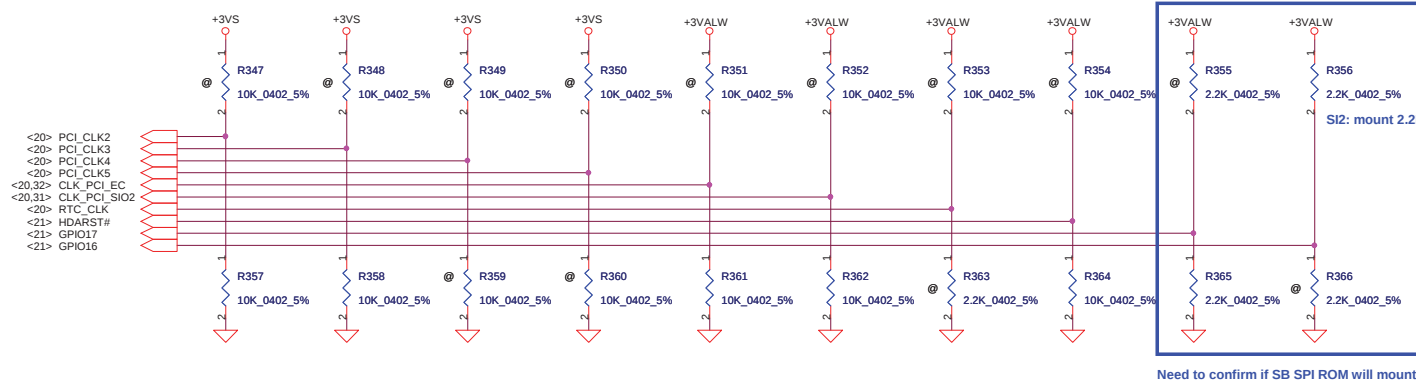


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Issued Date		2009-02-12		Deciphered Date		2009-02-12		Title					
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								Document Number		LA-5831P		Rev 1.0	
								Date		Wednesday, August 12, 2009		Sheet 23 of 44	
								Customer					

REQUIRED STRAPS

NOTE: SB700 HAS INTERNAL 15K PULL UP RESISTOR FOR RTC_CLK

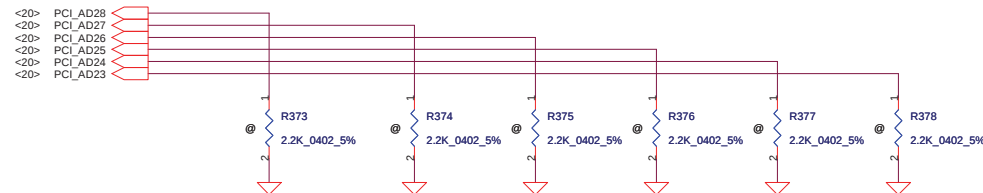
	PCI_CLK2	PCI_CLK3	PCI_CLK4	PCI_CLK5	LPC_CLK0	LPC_CLK1	RTC_CLK	AZ_RST_CD#	GP17	GP16
PULL HIGH	BOOTFAIL TIMER ENABLED	USE DEBUG STRAPS	RESERVED	RESERVED	ENABLE PCI MEM BOOT	CLKGEN ENABLED	INTERNAL RTC DEFAULT	EC ENABLED	Internal pull up H,H = Reserved H,L = SPI ROM	
PULL LOW	BOOTFAIL TIMER DISABLED DEFAULT	IGNORE DEBUG STRAPS DEFAULT			DISABLE PCI MEM BOOT DEFAULT	CLKGEN DISABLED DEFAULT	EXT. RTC (PD on X1, apply 32KHz to RTC_CLK)	EC DISABLED DEFAULT		



DEBUG STRAPS

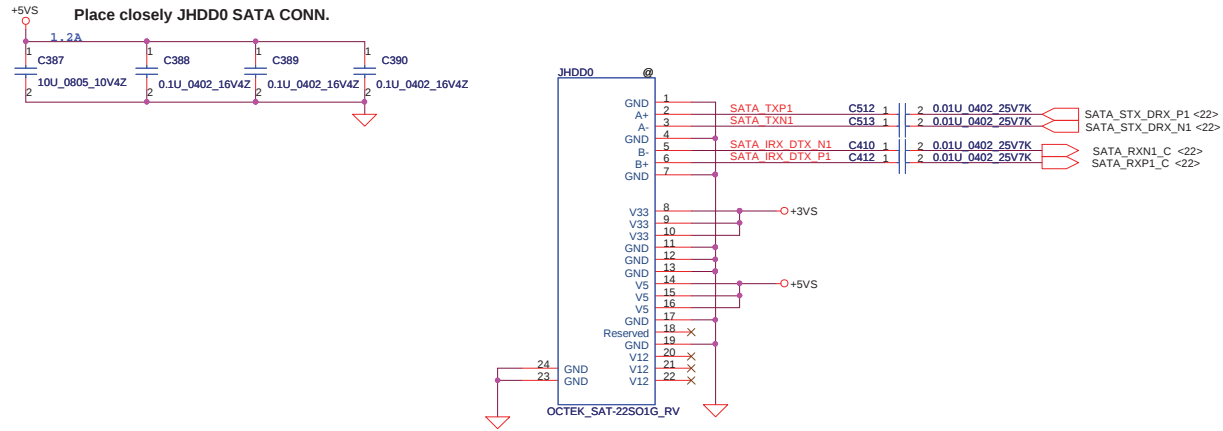
SB700 HAS 15K INTERNAL PU FOR PCI_AD[28:23]

	PCI_AD28	PCI_AD27	PCI_AD26	PCI_AD25	PCI_AD24	PCI_AD23
PULL HIGH	USE LONG RESET DEFAULT	USE PCI PLL DEFAULT	USE ACPI BCLK DEFAULT	USE IDE PLL DEFAULT	USE DEFAULT PCIE STRAPS DEFAULT	RESERVED
PULL LOW	USE SHORT RESET	BYPASS PCI PLL	BYPASS ACPI BCLK	BYPASS IDE PLL	USE EEPROM PCIE STRAPS	

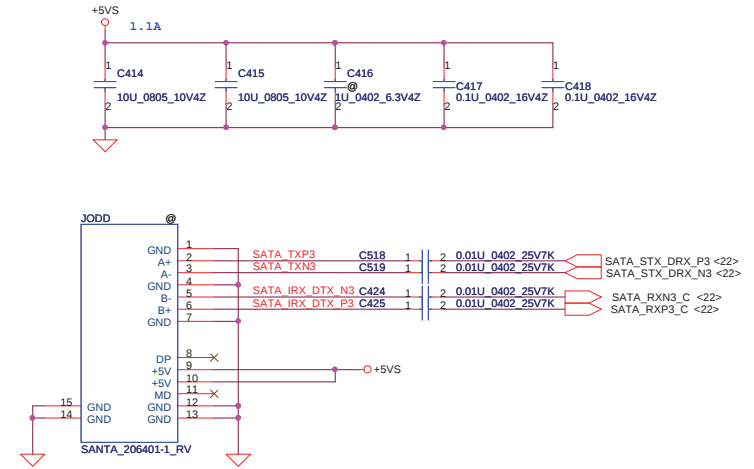


Security Classification	Compal Secret Data			Compal Electronics, Inc.	
Issued Date	2009-02-12	Deciphered Date	2009-02-12	Title	SB700 STRAPS
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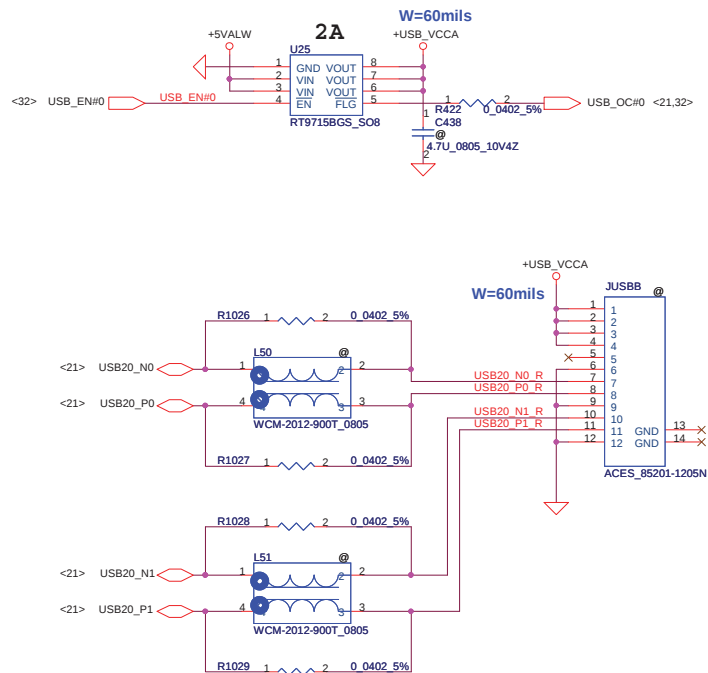
SATA HDD Conn.



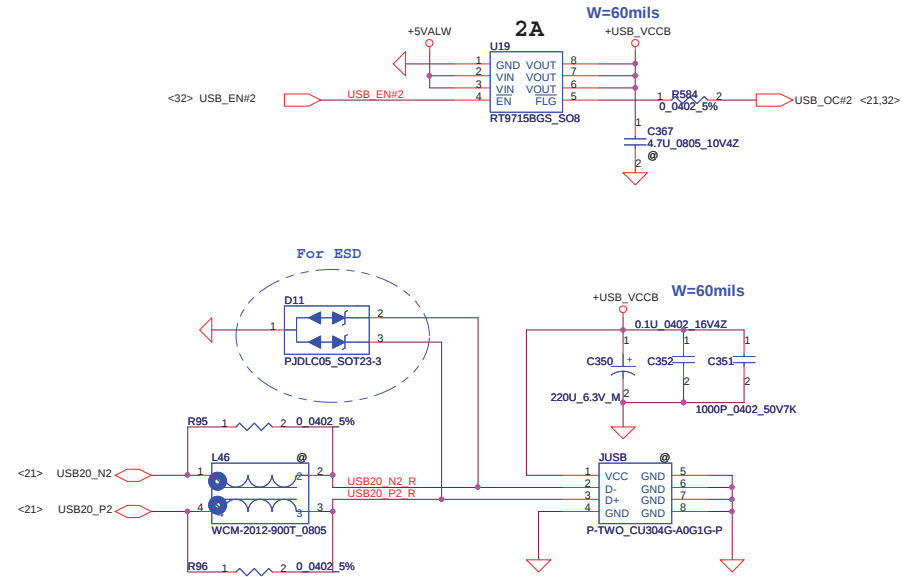
SATA ODD Conn



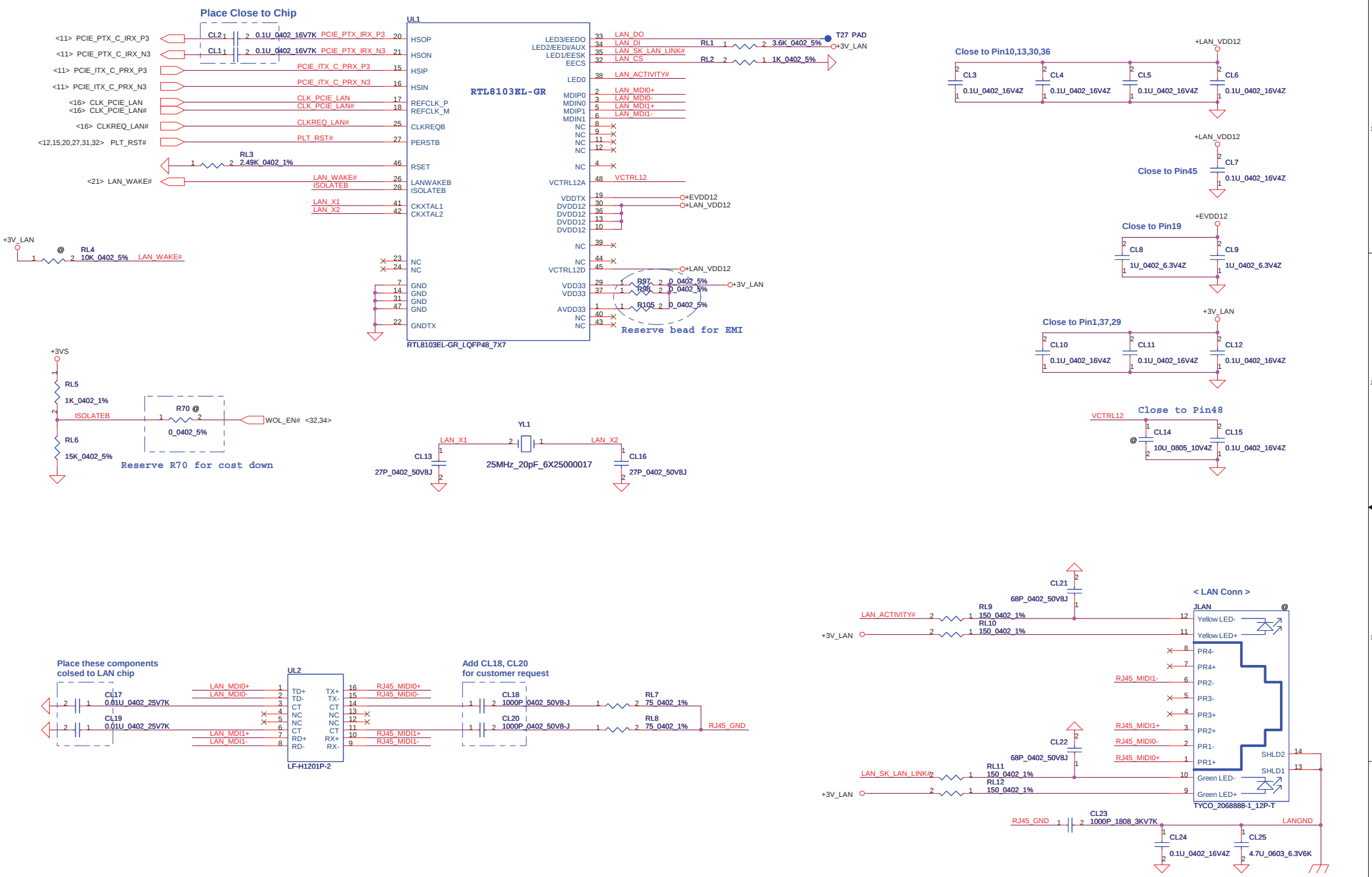
USB Right Board



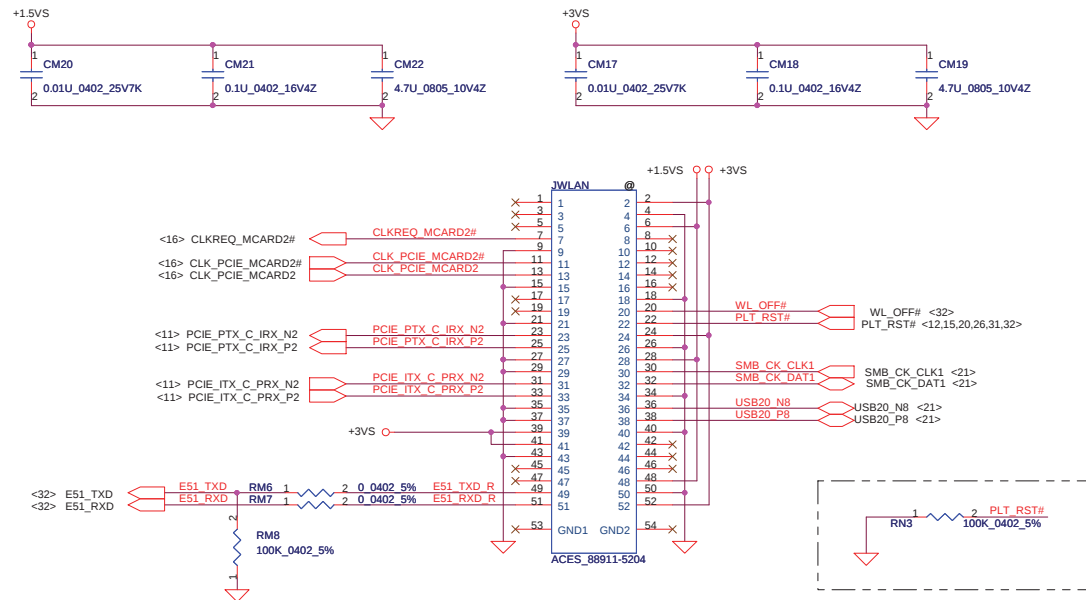
USB Left Conn



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								2009/04/14			
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				Document Number				LA-5831P			
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				Sheet				25 of 44			



< PCIe Mini Card for WLAN/ WiMAX >



<NB & SB >

< R1 for customer BOM STRUCTURE >

RS780MN



< R3 for mass production BOM STRUCTURE >

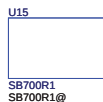
RS780MN



RS780MC



SB700

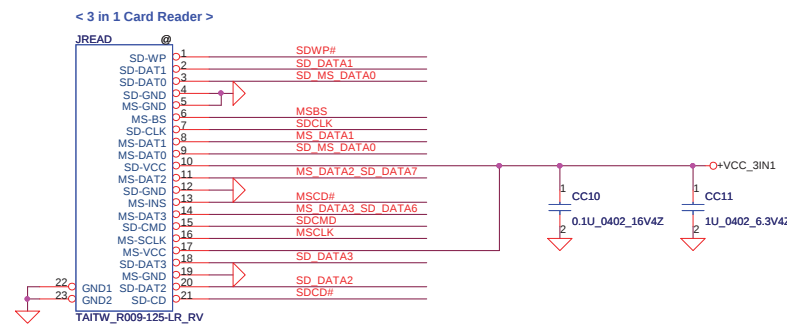
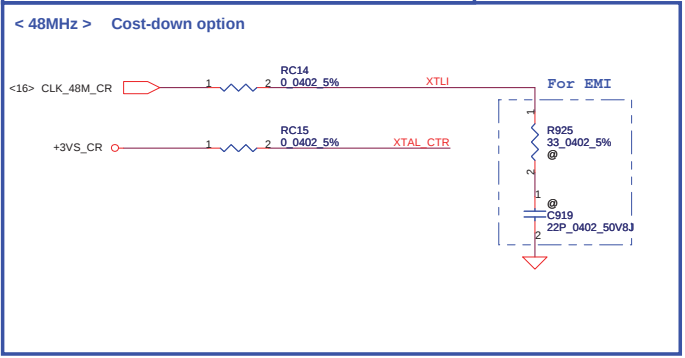
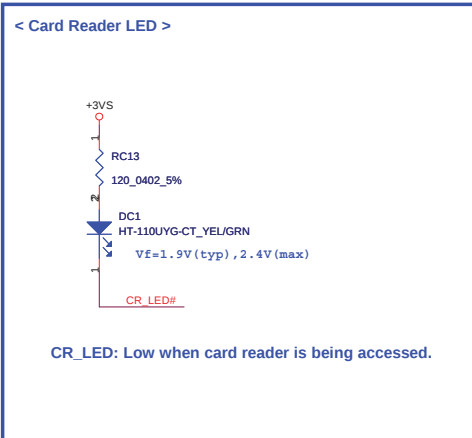
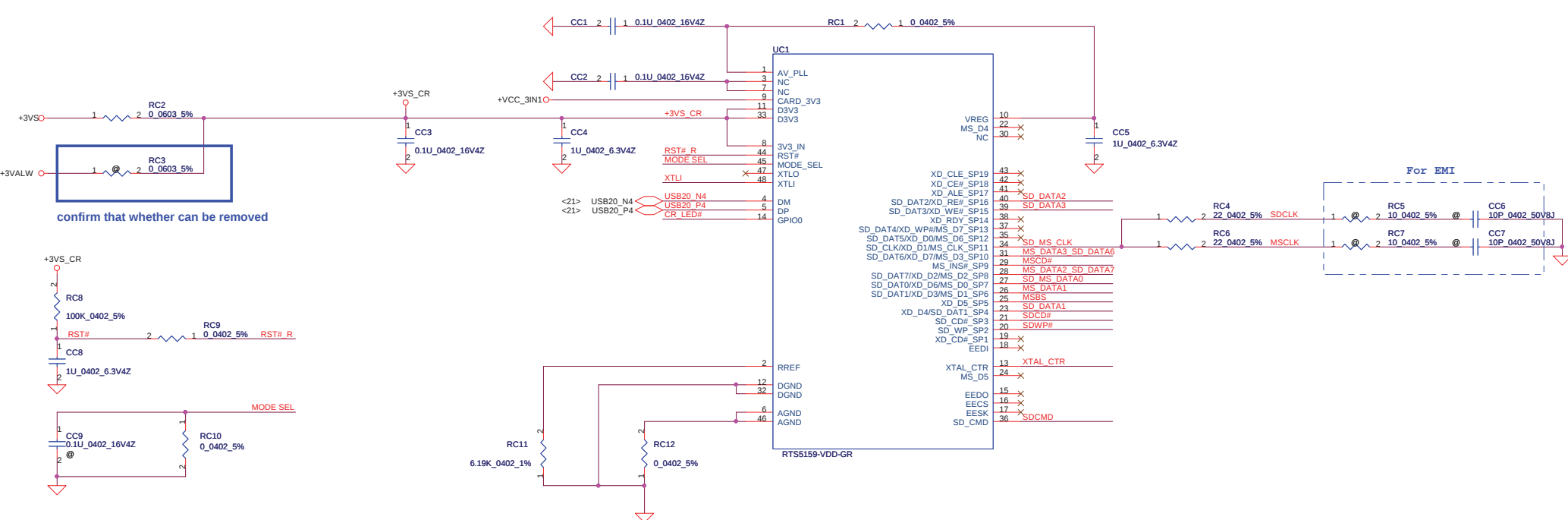


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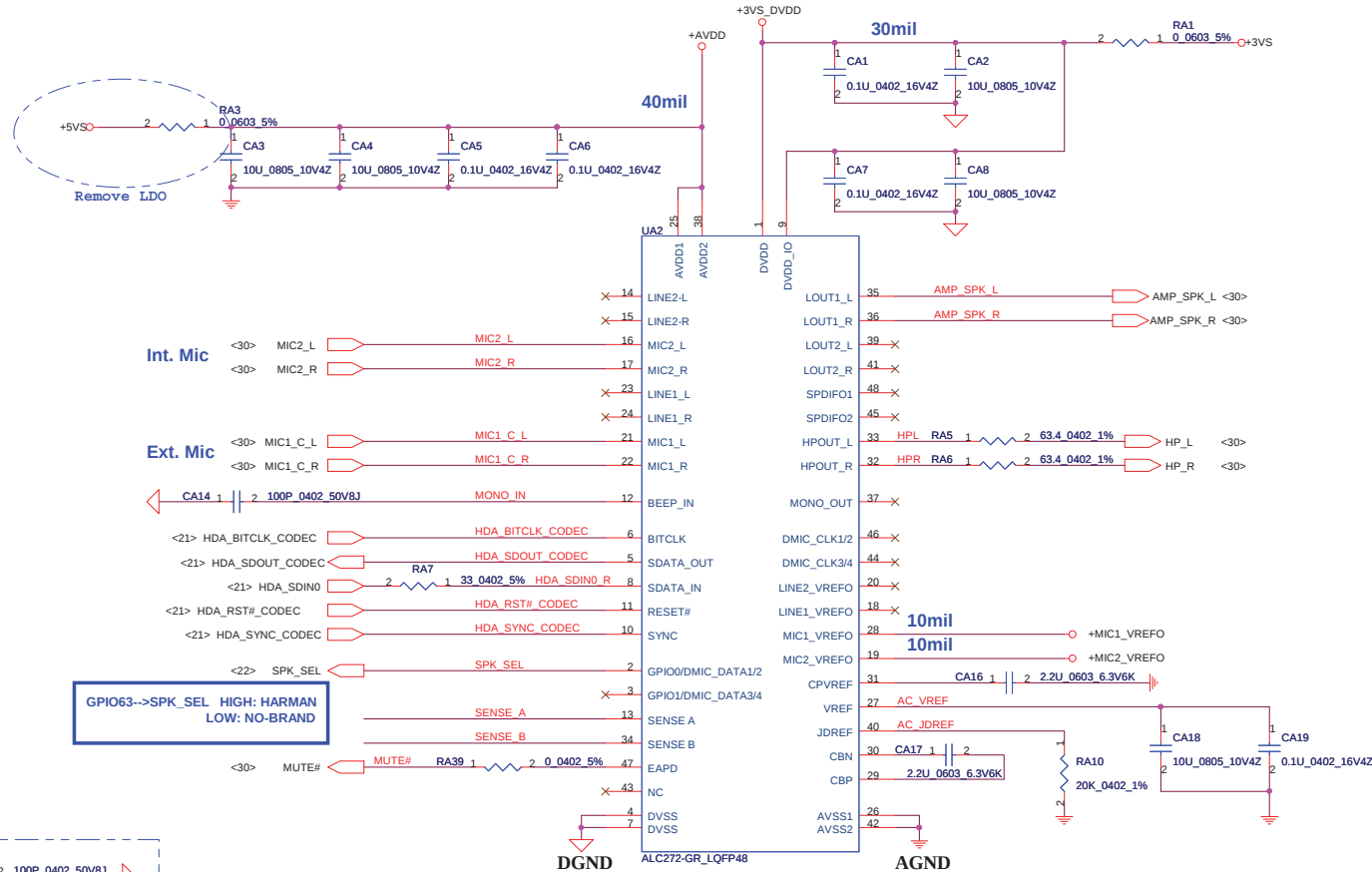
PCB



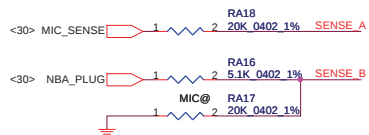
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Issued Date				2009-02-12				Title			
				Deciphered Date				WLAN / CAMERA			
								Document Number			
								LA-5831P			
								Rev			
								1.0			
								Date: Wednesday, August 12, 2009			
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R	C	USB AUTO DE-LINK	MS FORMATTER	Description
0	NC	YES		Recommended
NC	47P	YES	YES	Compatible with RTS5158E
NC	NC			LED ON
10K	180P			LED ON
10K	680P		YES	

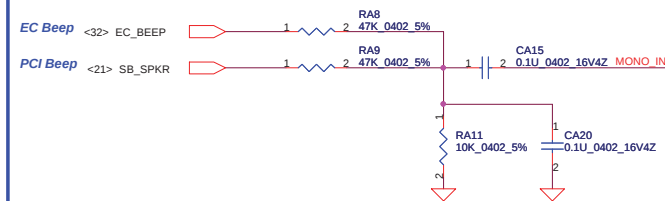


< SENSE_A & SENSE_B, place close to chip >



Sense Pin	Impedance	Codec Signals	Function
SENSE A	39.2K	PORT-A (PIN 39, 41)	
	20K	PORT-B (PIN 21, 22)	Ext. MIC
	10K	PORT-C (PIN 23, 24)	
	5.1K	PORT-D (PIN 35, 36)	SPK out
SENSE B	39.2K	PORT-E (PIN 14, 15)	
	20K	PORT-F (PIN 16, 17)	Int. MIC
	10K	PORT-H (PIN 37)	
	5.1K	PORT-I (PIN 32, 33)	Headphone out

< MONO_IN SOURCE >



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Compal Electronics, Inc.

HD Audio ALC272 Codec

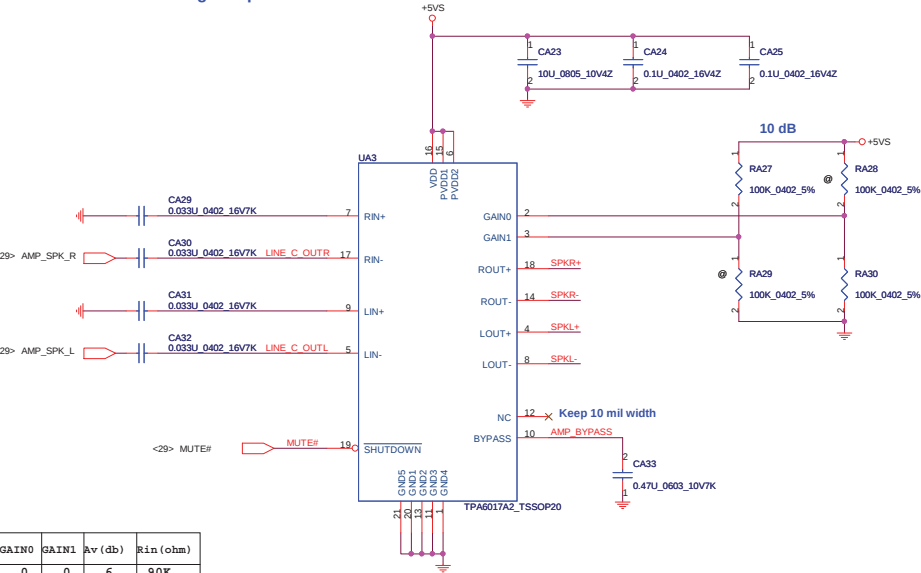
LA-5831P

Rev 1.0

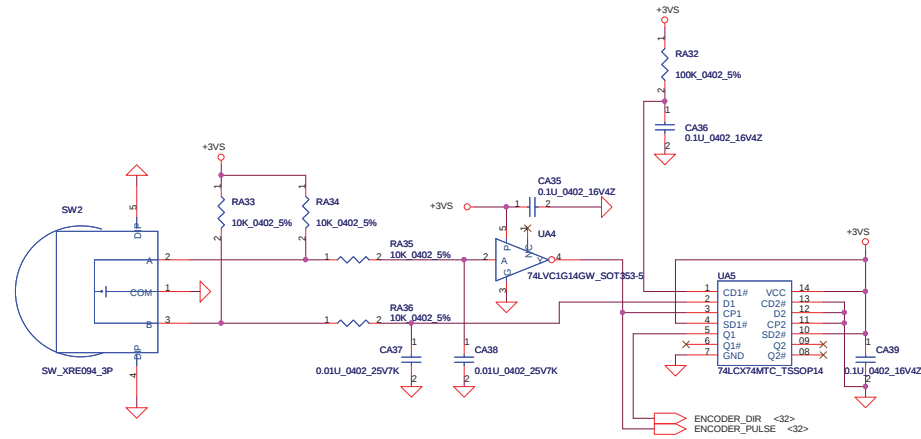
Wednesday, August 12, 2009

Sheet 29 of 44

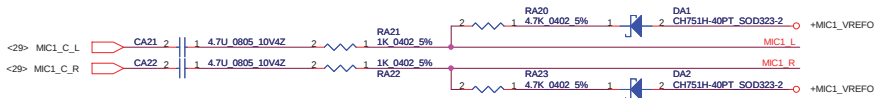
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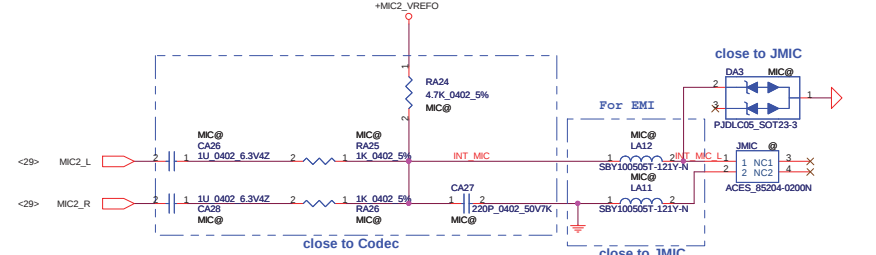
< Volume Control >



< Ext. Mic >

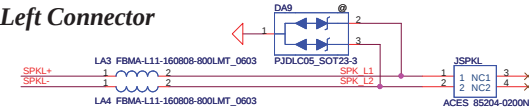


< Int. Mic >

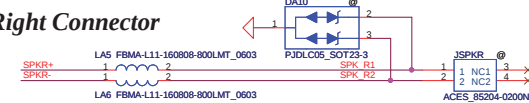


< Speaker Connector >

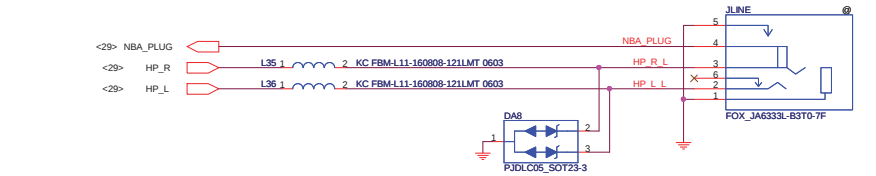
Left Connector



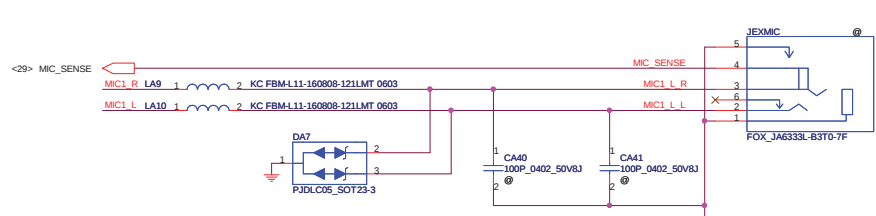
Right Connector



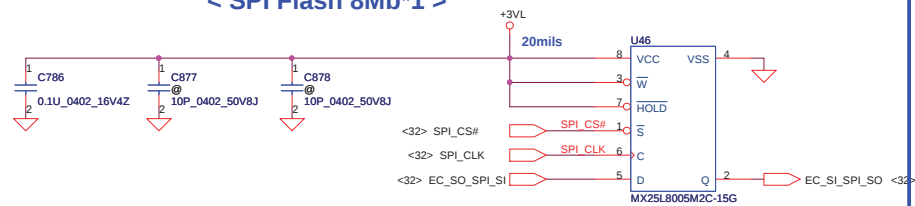
< HeadPhone JACK >



< Ext.MIC/LINE IN JACK >

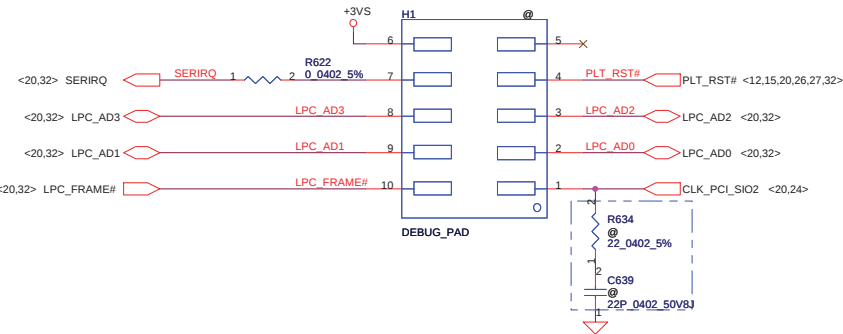


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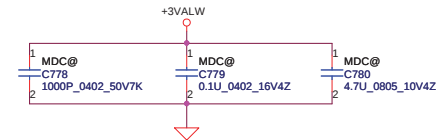
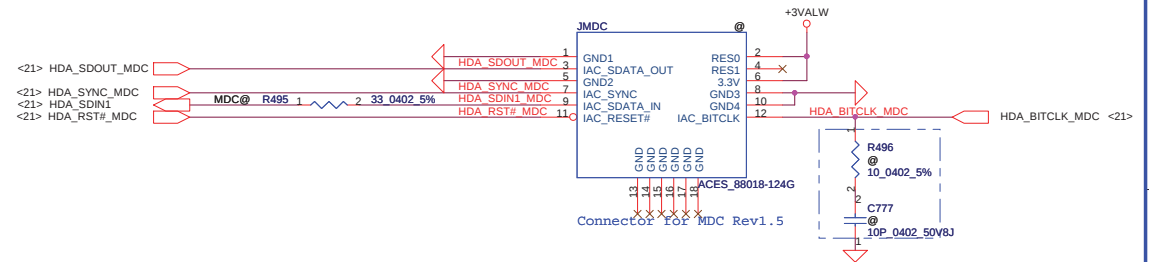


< LPC Debug Port >

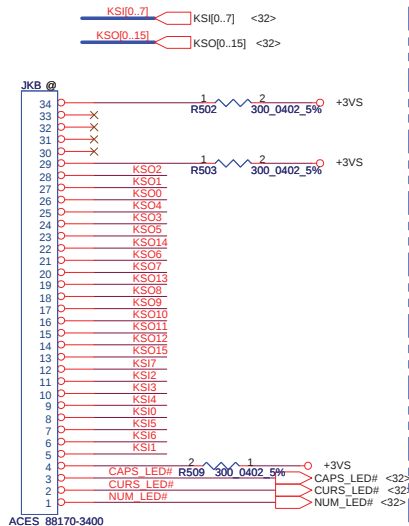
Please place the PAD under DDR DIMM.



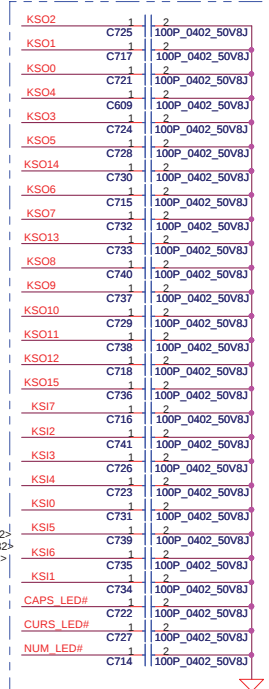
< MDC 1.5 Conn >



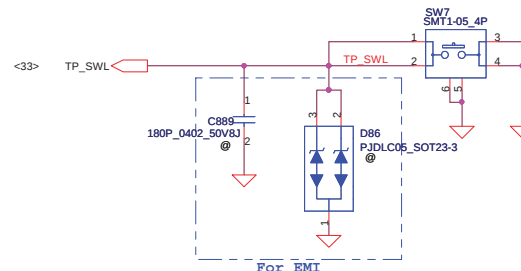
KEYBOARD CONN.



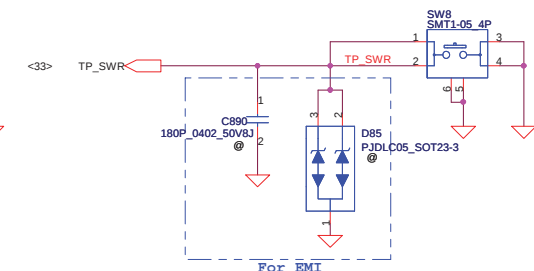
For EMI



Left switch

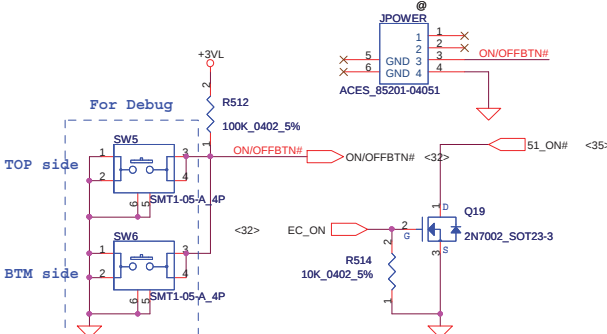


Right Switch

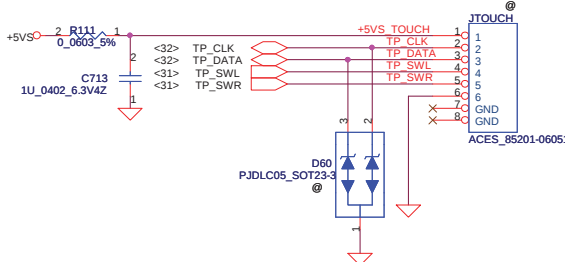


Security Classification				Compal Secret Data				Title			
Issued Date				Deciphered Date				Document Number			
2009-02-12				2009-02-12				SPI / LPC / MDC			
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Date: Wednesday, August 12, 2009				Sheet				Rev 1.0			
				31				of 44			

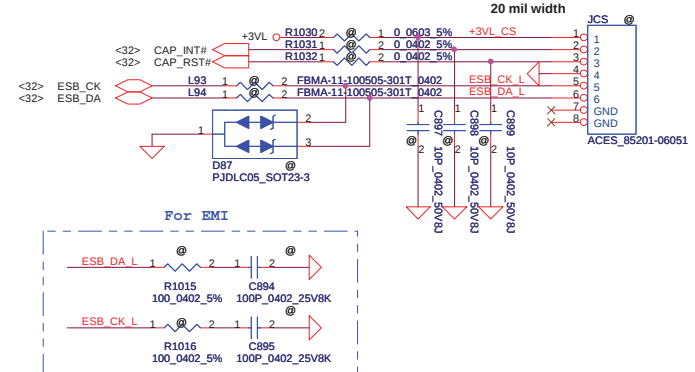
Power Button/ PWR/B



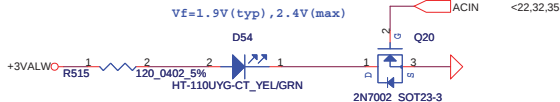
Touch Pad Connector



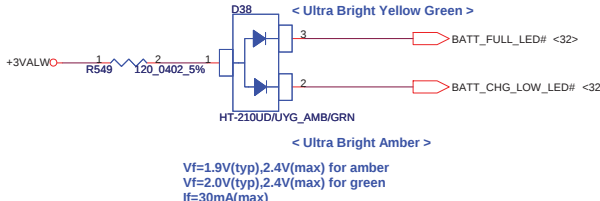
CS/B Connector



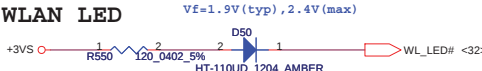
DC-IN LED



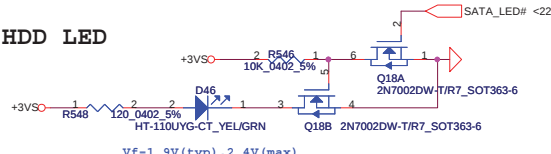
BATT CHARGE/FULL LED



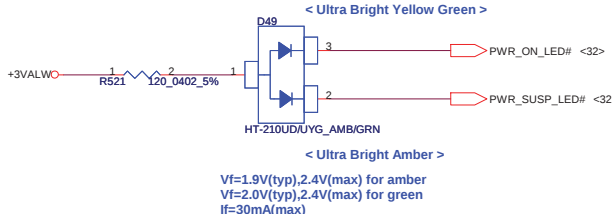
WLAN LED



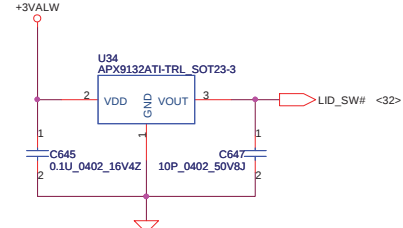
HDD LED



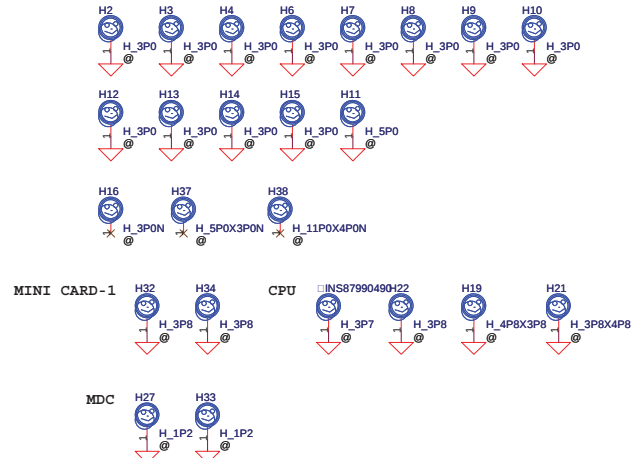
POWER ON/SUSPEND LEI



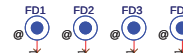
Lid SW



Screw Hole

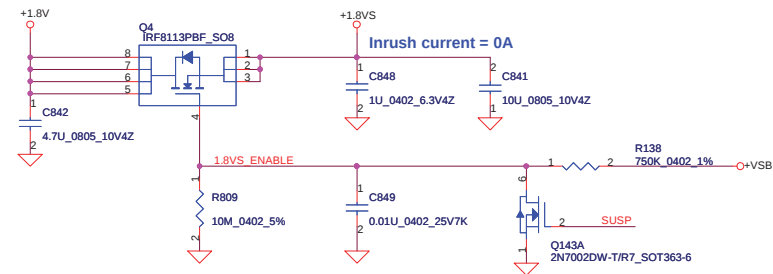


PCB Fedical Mark PAI

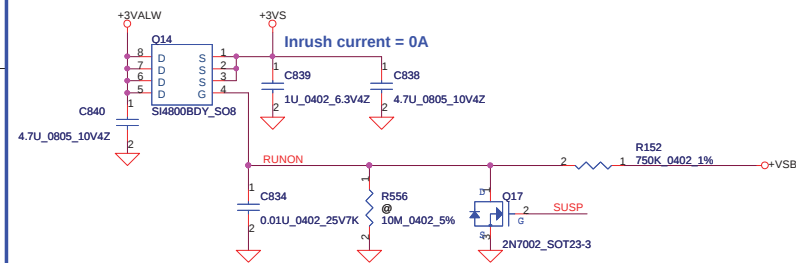


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					Part	Document Number	Rev
					Custom	LA-5831P	1.0
					Date:	Wednesday, August 13, 2009	Sheet

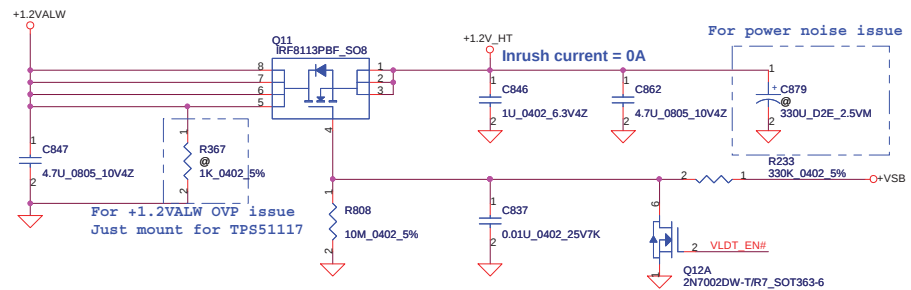
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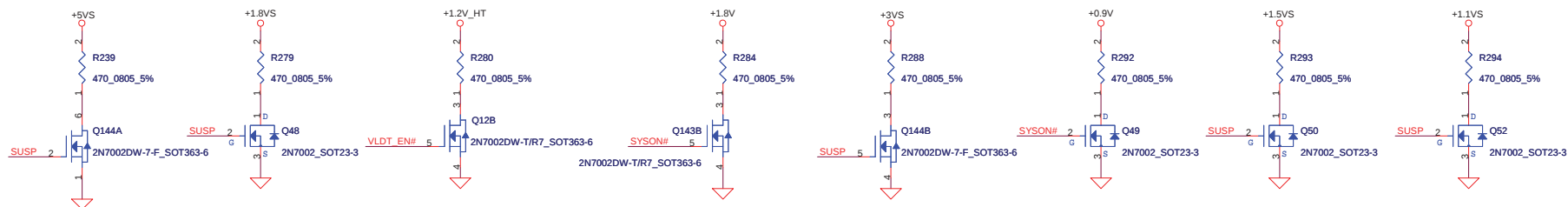
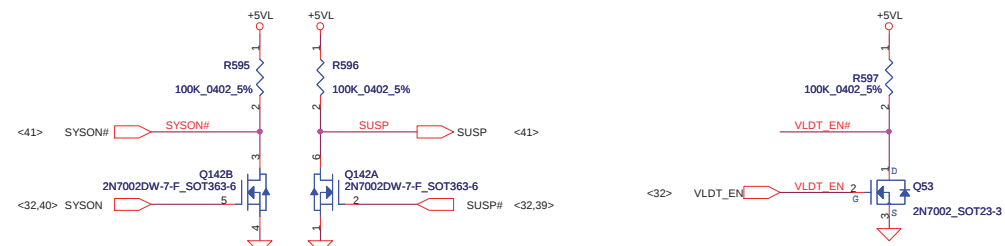
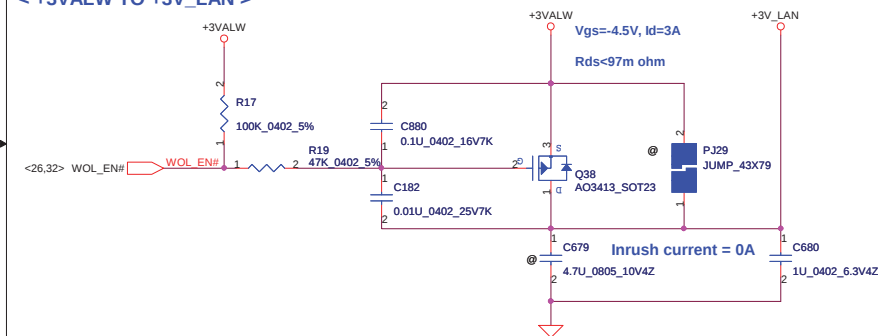
< close to PQ20, must EMI confirm >



< +1.2VALW TO +1.2V_HT >



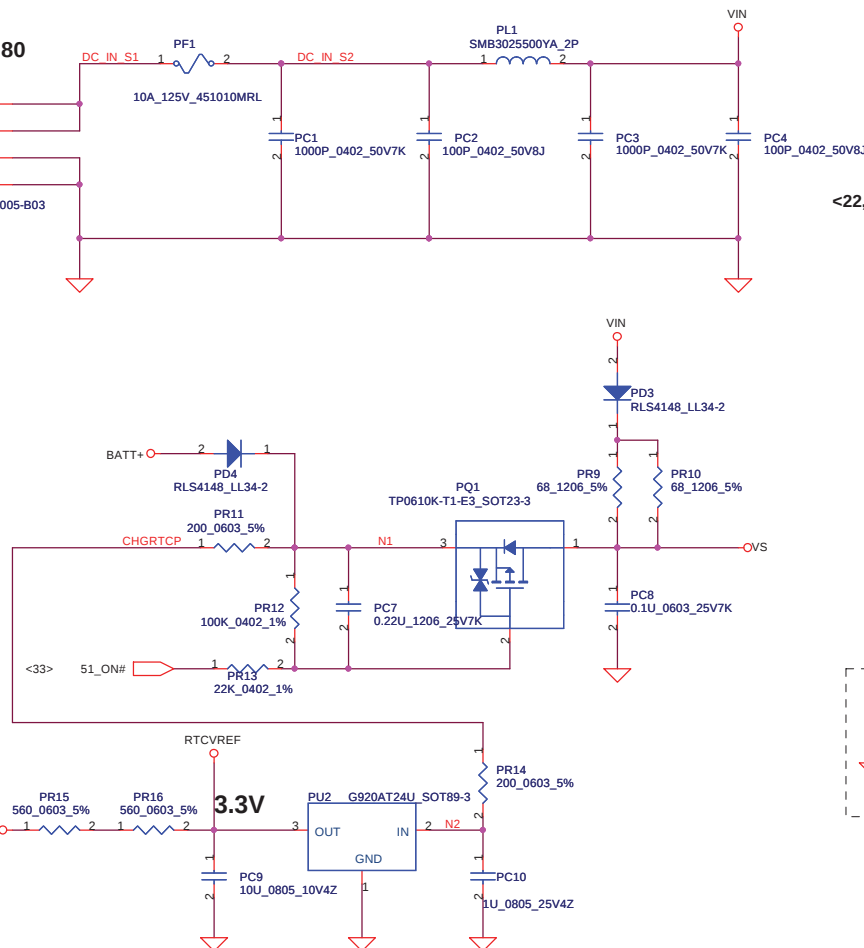
< Inversion of SYSON, SUSP#, VLDT_EN, EC_ON >



Compal Electronics, Inc.			
Title			
DC / DC Circuits			
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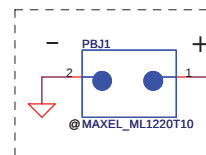
DC301001M80

@SINGA_2DW-0005-B03



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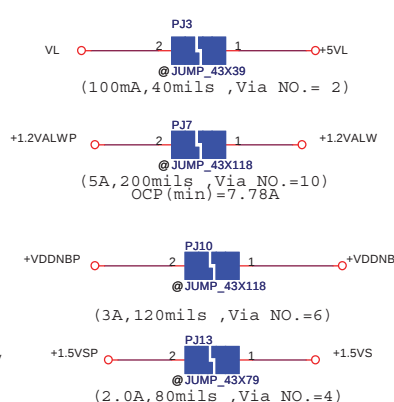
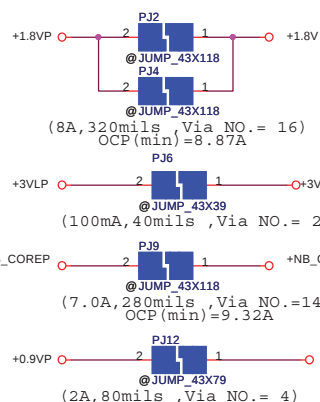
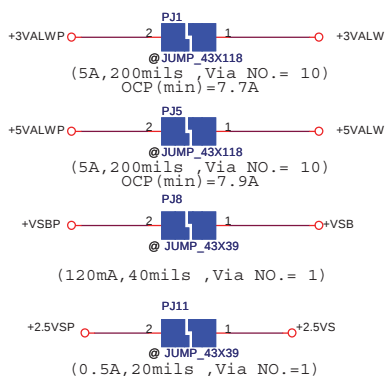
RTC Battery



SP093MX0000

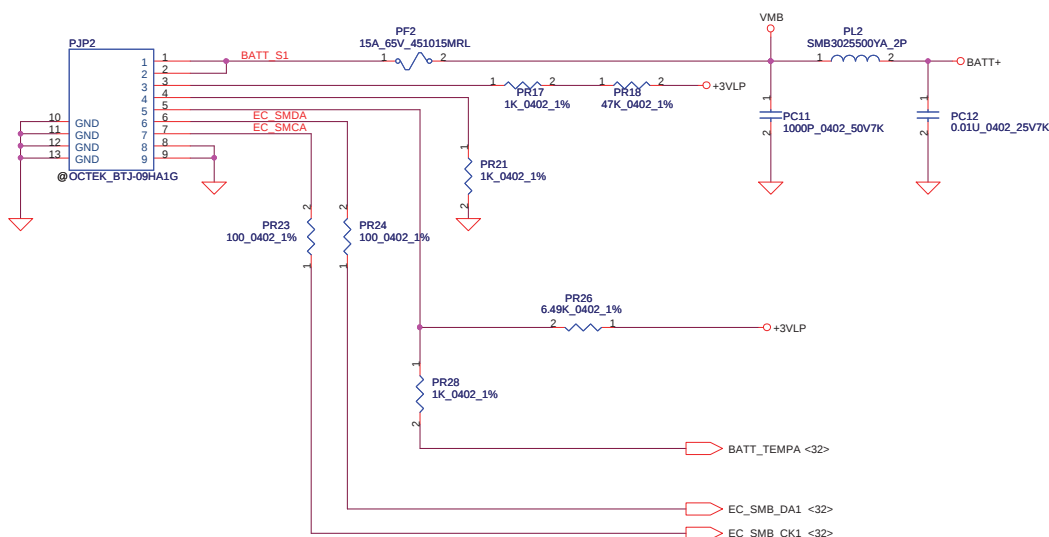
Vin Detector

High	18.384	17.901	17.430
Low	17.728	17.257	16.976

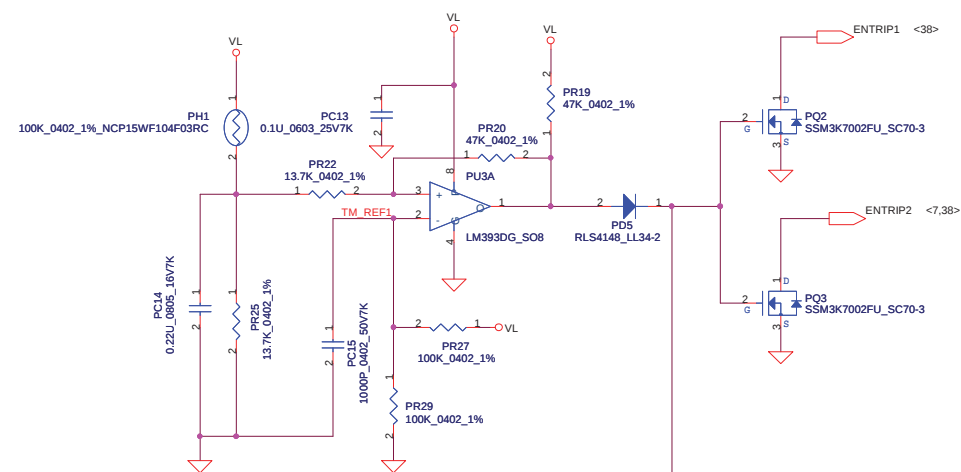


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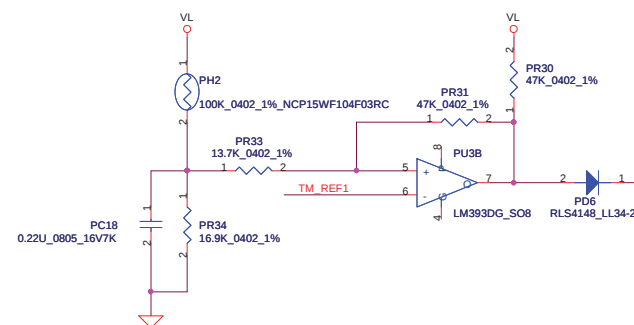
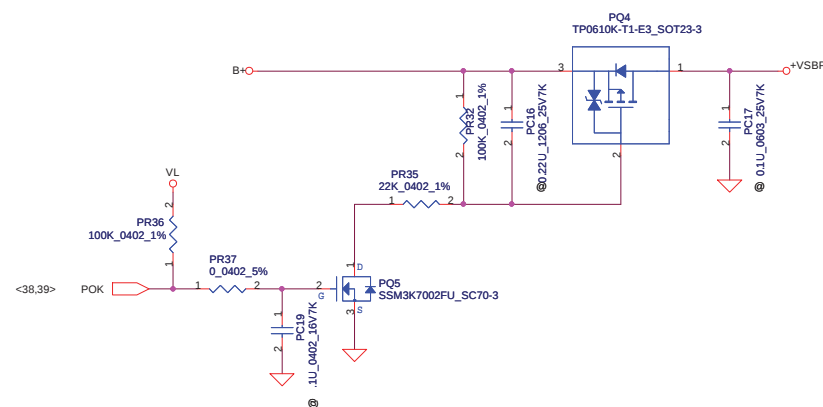
Compal Electronics, Inc.			
Title			
DCIN/DECTOR			
Size	Document Number	Rev	
	LA-5831P	1.0	
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PH1 under CPU bottom side :
CPU thermal protection at 92 degree C
Recovery at 56 degree C

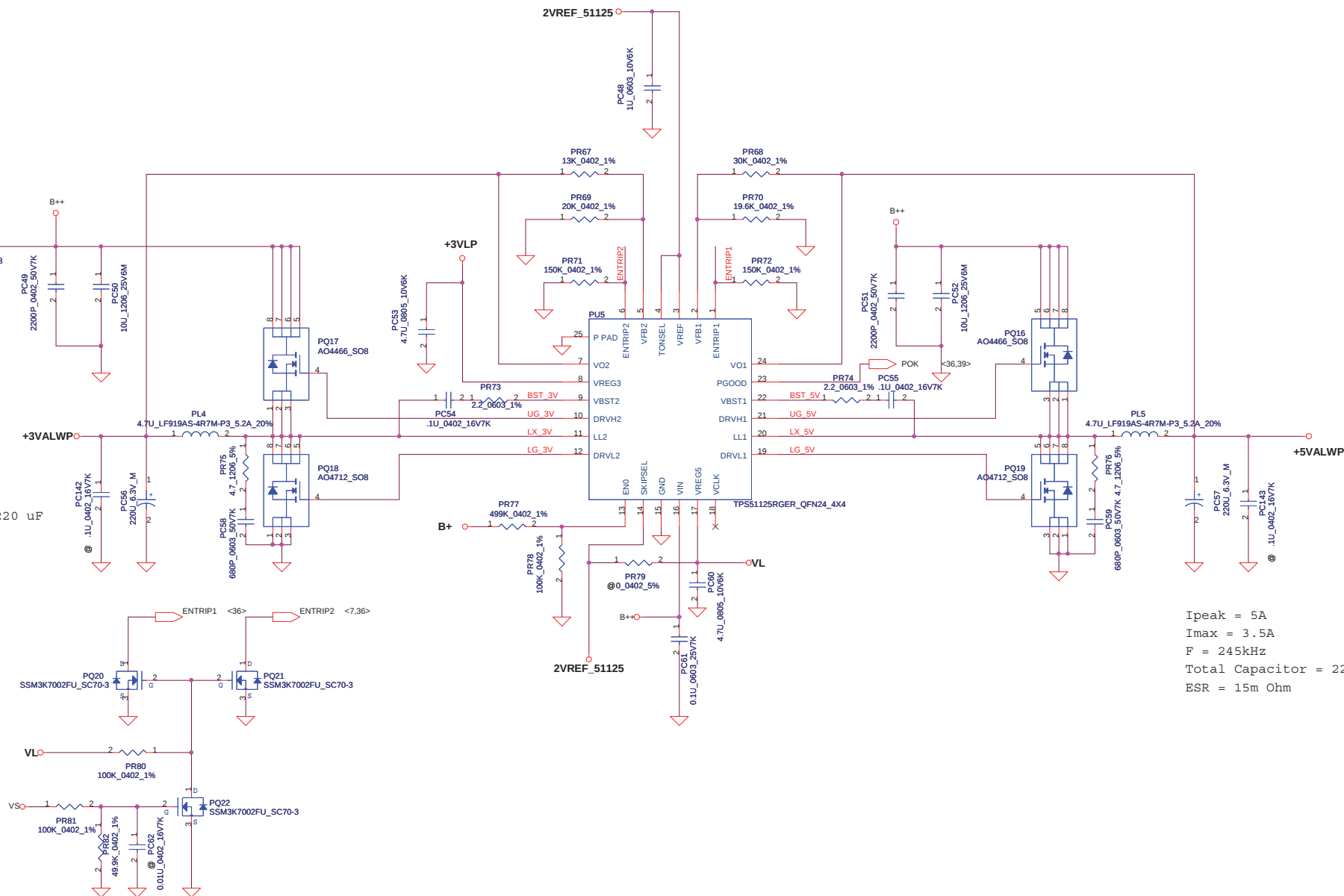


PH2 near main Battery CONN :
BAT. thermal protection at 92 degree C
Recovery at 56 degree C



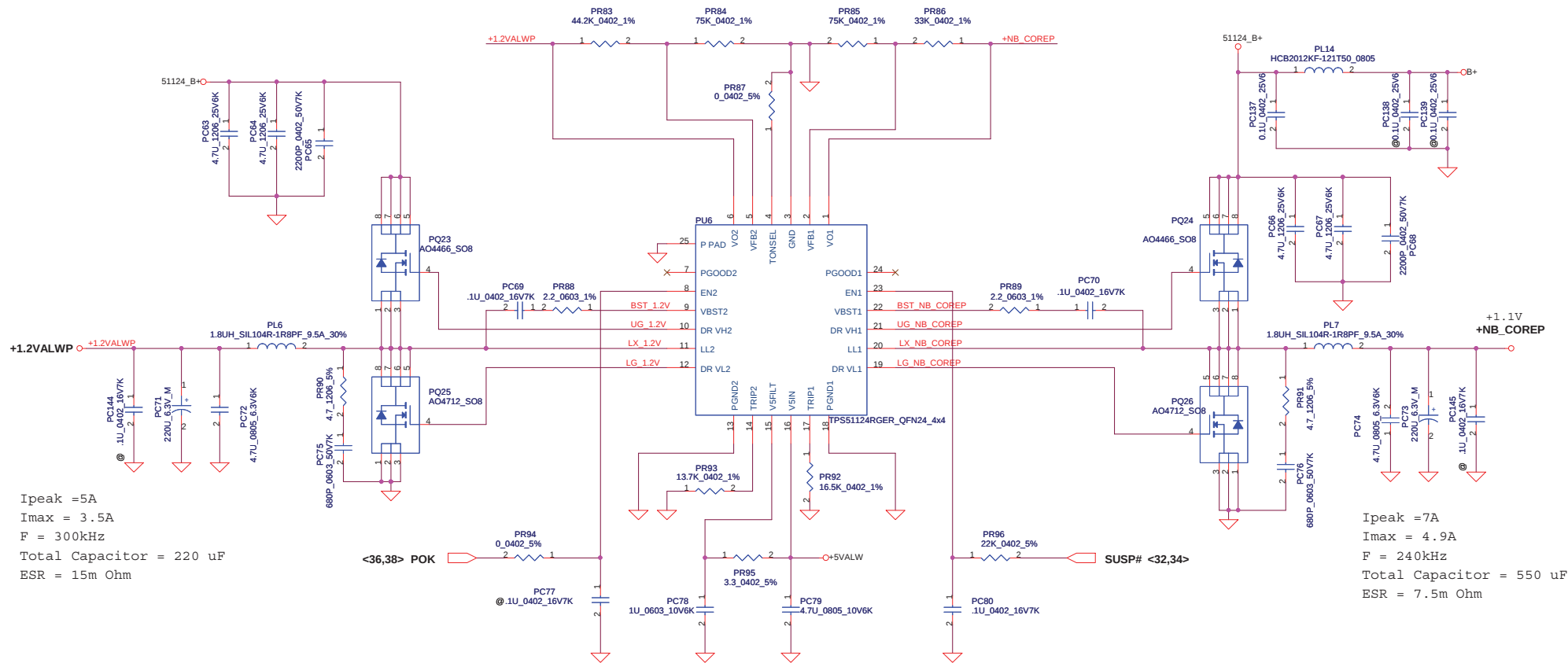
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Ipeak = 5A
 Imax = 3.5A
 F = 305kHz
 Total Capacitor = 220 uF
 ESR = 15m Ohm

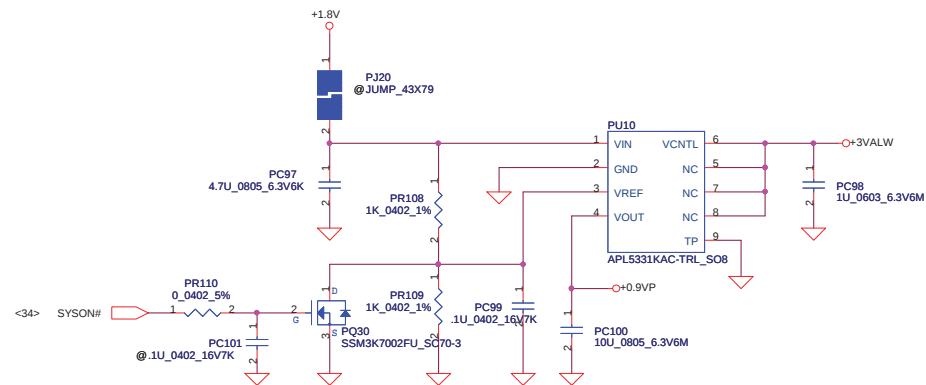
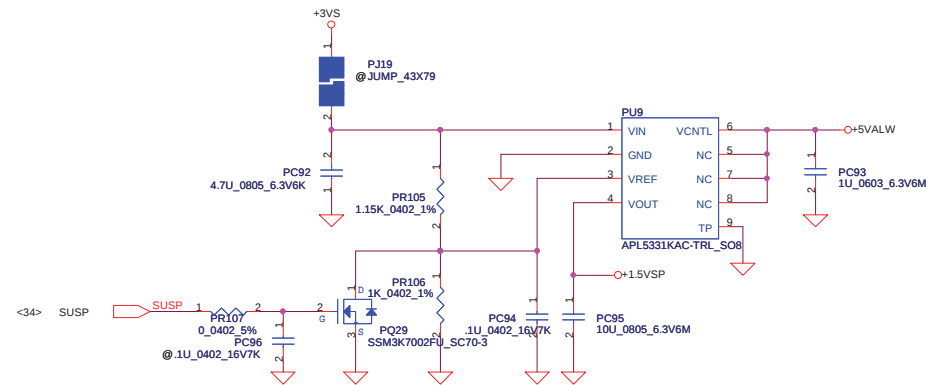


Ipeak = 5A
 Imax = 3.5A
 F = 245kHz
 Total Capacitor = 220 uF
 ESR = 15m Ohm

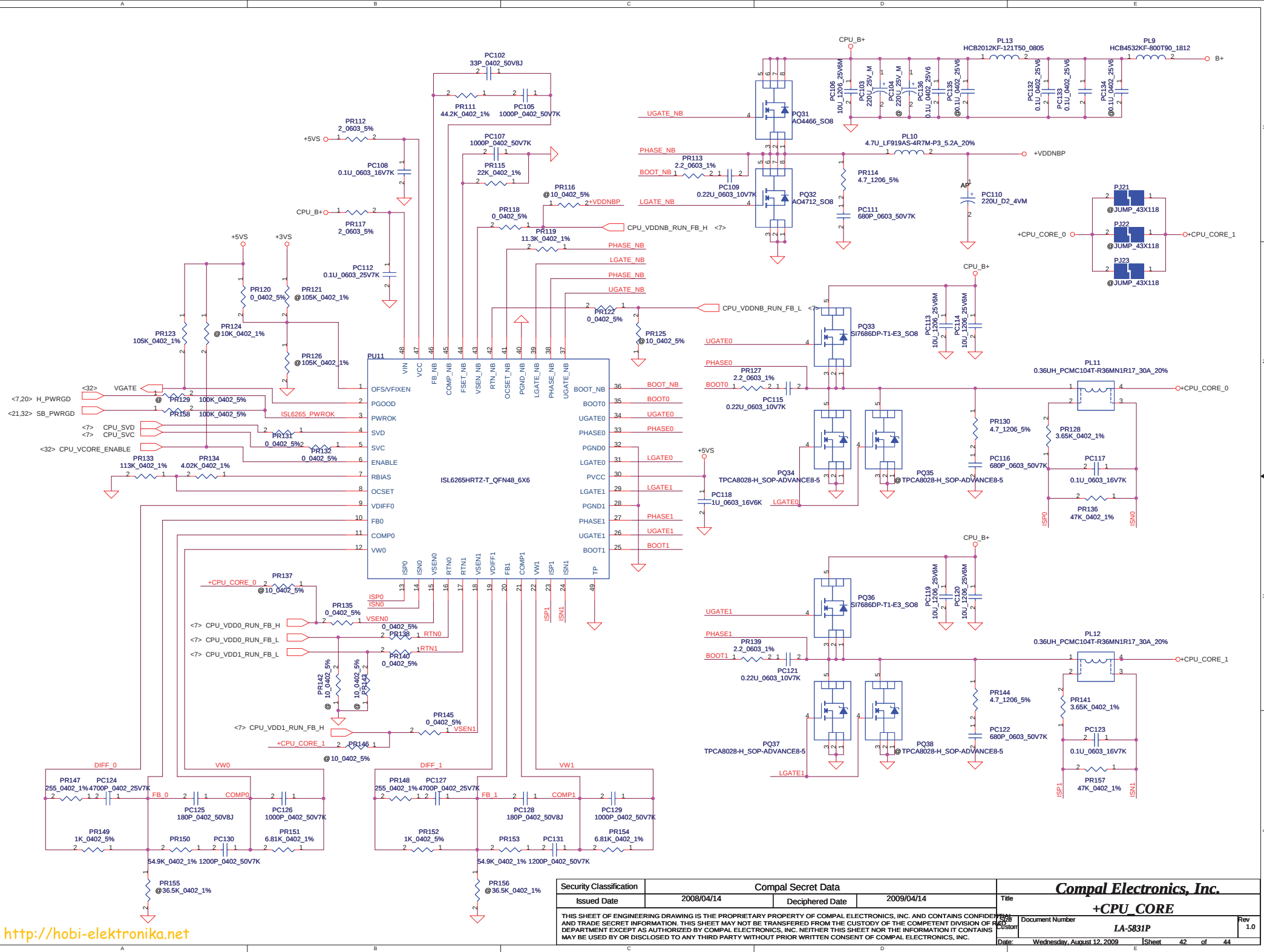
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Issued Date	2008/04/14	Deciphered Date	2009/04/14	3VALW/5VALW
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Version Change List (P. I. R. List) for Circuit

Item	Page#	Title	Date	Request Owner	Issue Description	Solution Description	Rev.
35-43	PVT		2009/07/20	Release			
39-40	Pre-MP	Change PL6, PL7, PL8	2009/07/30	POWER		Change PL6, PL7, PL8 footprint	
38	Pre-MP	Change PC56	2009/07/30	POWER		change to common circuit.	
36	Pre-MP	Change PH1, PH2	2009/08/4	POWER		change to common circuit.	
37	Pre-MP	Change PR38, PR45	2009/08/4	POWER		change to common circuit.	

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Issued Date	2009-02-12	Deciphered Date	2009-02-12	Title	List History	
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				Date: Wednesday, August 12, 2009	Sheet	43 of 44

HW4 Product Improvement Record (P.I.R.)

NEWAE LA-5831P SCHEMATIC CHANGE LIST
REVISION CHANGE: 0.1 TO 1.0
GERBER-OUT DATE: 2009/08/07

NO	DATE	PAGE	MODIFICATION LIST	PURPOSE
1	7/21	25	Change JUSBB footprint back to E-T_6905-E12N-00R_12P	USB/B pin define reverse
2	7/27	25	Add D11	For ESD
3	7/27	9	Add C120-C123	For EMI
4	7/27	32	Add R344 and R345	For EC pin damage issue
5	7/30	26	Add R97, R98 and R105	Reserve bead for EMI
6	8/6	18	Add C707, C708 and C709	For EMI
7	8/6	30	Add LA11 and LA12	For EMI
8	8/6	32	Add R74 and C607	For EMI
9	8/6	31	Delete R518 and C606	For EMI
10	8/6	7	Add C124	For EMI

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				Rev 1.0	
Date: Wednesday, August 12, 2009				Sheet	44 of 44